



***AIN SHAMS UNIVERSITY – FACULTY OF ENGINEERING (ASU – FoE)***

**IN COLLABORATION WITH**



***SCHOOL OF ARCHITECTURE, COMPUTING AND ENGINEERING***

***BEng (Hons) in  
Communication Systems Engineering***

**Programme Handbook**

**Academic Year 2019-2020**

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## 1 WELCOME AND INTRODUCTION

This handbook is intended for all students taking the BEng (Hons) Communication Systems Engineering Double Degree award from Ain Shams University and the University East London. You will find it a useful source of information at the start of your programme, but you should also keep it for reference purposes throughout your time here. It is, however, not intended to provide all you need to know, so you should also read Ain Shams University Handbook.

The partnership with University East London (UEL), our collaborative partner in the UK, started in Fall 2018. Three ASU Credit Hours programmes (Building Engineering Programme, Environmental Architecture and Urbanism Programme, and Computer Engineering and Software Systems Programme) have been validated and Double Degree awarded by ASU and UEL (<https://eng.asu.edu.eg/UEL>).

UEL is an internationally renowned university aiming to achieve the highest possible standard of academic excellence. Apart from being one of the UK's most diverse and fastest growing universities, UEL is a global learning community with internationally recognised research. We are most confident that our collaboration with UEL will yield significant academic benefits both for ASU as an institution, and for the students who will enrol the BEng (Hons) Communication Systems Engineering programme.

Our vision at Faculty of Engineering -Ain Shams University (FoE-ASU) is being one of the best colleges known for their leadership regionally and internationally in engineering education and scientific research through interdisciplinary and unique academic programmes that meet the needs of the community and contribute to sustainable development. It aims for preparation of distinguished graduates capable of keeping pace with global technology in various disciplines that meet the needs of local and regional markets and can conduct scientific research. This is applied through the creation of appropriate conditions for faculty members and their assistants and students, and through providing educational programmes in advanced undergraduate studies, as well as establishing advisory centres and research labs which include sophisticated contribution to community service to meet its needs.

Credit Hours Engineering Programmes at the Faculty of Engineering - Ain Shams University (ASU- ICHEP) is one of the outstanding models for engineering education in Arab Republic of Egypt, as it seeks to provide high-quality of engineering education based on interdisciplinary programmes and the application of international standards of credit hours systems followed in the most prestigious universities in the world. Learning environment at ASU- ICHEP focused on the graduation engineers equipped with skills, knowledge, and the ability to life-long learning. ICHEP began at the Faculty of Engineering - Ain Shams University in 2006 with two programmes namely Building Engineering and Materials Engineering with a number of students that does not exceed 60 students.

There are nine programmes (Building Engineering, Communication Systems Engineering, Materials Engineering, Manufacturing Engineering, Energy and Renewable Energy Engineering, Computer Engineering and Software Systems, Landscape Architecture, Mechatronics Engineering and Automation, and

Environmental Architecture & Urbanism Engineering). Thus, currently the total number of students in ASU- ICHEP exceeds 3000 students that work side by side with Specialized programmes. ASU- ICHEP is characterised by adopting new models in learning, which are different from the traditional system in Egyptian Engineering colleges. Moreover, the curriculum adopted in ASU- ICHEP are inspired by the vision of experts and specialists in these fields. The programmes in ASU- ICHEP follow mainly the National Authority for Quality Assurance and Accreditation of Education (NAQAAE).

The framework for NAQAAE was established to enhance the quality of education in Egypt with a mandate to ensure the development of basic reference standards for education - National Academic Reference Standards (NARS). The Communication Systems Engineering programme is designed based on the NARS requirements to inspire students to be innovative and creative by using appropriate teaching and learning technologies and pursuing independent and life-long learning.

Our graduates are expected to have productive and very rewarding careers in a variety of capacities. The programme enables graduates to enter the following markets: Telecommunication, Satellite communication, Broadcasting, Cellular Networks, Internet Network, Microwave, Optical Network, Electronics, and Embedded Systems

We are confident that you have made the right choice to continue your lifelong learning journey with ASU. We promise to make your time here with us a most enriching educational experience for you.

**Mohamed Abouelatta, PhD**  
Programme Leader

## **2 INTRODUCTION TO THE PROGRAMME**

### **Programme Philosophy**

The Communication Systems Engineering Programme was launched at Ain Shams University in 2007, and is aiming to graduate engineers with the ability to deal with the latest developments in the fields of advanced communication, optical and electronic systems to meet the requirements of the market at the moral and professional levels by creating the appropriate conditions for the development of different skills of students and faculty members and cooperate with specialized industrial and research bodies locally and internationally.

The students are exposed to a wide variety of courses to build an open scope to telecommunication engineering which is interdisciplinary in nature. The graduate acquires his degree by taking a balanced curriculum that is pre-dominantly concerned with communication systems on different levels and which does not neglect required basic sciences used in this field.

The BEng (Hons) Communication Systems Engineering degree will give students an advantage in the employment market where the content of the programme provides a broader range of skills. Furthermore, a validated degree via a UK HEI will provide the students with a richer competency and skills-set. Finally, the skills which the students will gain on the programme will enhance the Communication Engineering discipline in Egypt and build capacity for sustainable development.

### **2.1 Programme duration and modes of study**

The Communication Systems Engineering programme is a 4-year full-time or 8 year part-time mode. The programme includes a foundation year and three (3) years for the specialised courses.

In senior years (Level 5 & 6), students choose among three tracks of specialisation: Signals and Communication Systems, Circuits and Systems, and Physical and Wave Electronics. They study the specific specialisation courses corresponding to the chosen pathway, and there are offered wide range of technical electives that students can choose from according to their field of interest, and their ambition in their future career. The programme awards a Bachelor degree in the field of study.

The allowed study duration on the programme is four years for full-time mode. In exceptional circumstances, this time limit may be extended to five (5) years, which does not include frozen semesters for reasons acceptable by the faculty, after which the student is expelled from the programmes.

The students are allowed to register fewer number of modules to comply with Part time mode of UEL with maximum study duration of eight years after first enrolment on the programme.

It is possible to move from full-time to part-time study and vice-versa to accommodate any external factors such as financial constraints or domestic commitments. Many of

our students make use of this flexibility and this may impact on the overall duration of their study period and the fees students pay annually, depending on the agreed financial arrangements.

## **2.2 Programme aims and objectives**

The programme aims at generating a graduate who is well trained in modern telecommunication industry as well as having a background in communication systems that enables him/her to fit easily within a modern telecommunication work environment and be able to identify market needs in this fast-moving segment of business.

The specific aims of the programme are:

- Develop in the students a strong understanding of the capabilities and limitations of communication systems and the related electronics.
- Give the students a chance to gain knowledge and develop skills in a wide variety of specialized selective courses.
- Develop advanced analytical and experimental skills that will allow the successful graduates to design new communication systems and provide them with the skills to critically analyse existing designs.
- Develop students' competence in computing in terms of software engineering and the use of the latest computing technologies.
- Train students in laboratory techniques for the safe and effective construction and testing of communication systems.
- Develop students' excellence in communication of technical and non-technical information in written, oral or graphical form and the duties associated with the status of a communication engineer.
- Provide the students with opportunities for internships in industry to gain career enhancing experience of the application of engineering principles.
- Enhance the active learning by the students and provide them with a well-developed academic base and ethics that provides for further learning and professional development

## **2.3 Programme Intended learning outcomes (ILO's)**

The graduates of the Communication Systems Programme should be able to demonstrate:

### **Knowledge**

- Concepts and theories of mathematics, sciences, electronic engineering systems and their application within the field of Communication engineering.
- Quality assurance systems, codes of practice and standards, health and safety requirements and environmental issues associated with Communication engineering.

- Professional ethics and impacts of engineering solutions on society and environment.
- Coding and decoding techniques, Basics of electromagnetism, wave propagation and Transmission lines for communication systems.
- Analysis and control and Basics of Communication systems and networks.

### **Thinking skills**

- Select appropriate mathematical and computer-based methods for modelling and analysing communication systems problems
- Think in a creative and innovative way in problem solving and design.
- Combine, exchange, and assess different ideas, views, and knowledge from a range of sources.
- Solve industrial problems, often on the basis of limited and possibly contradicting information.
- Incorporate economic, societal, environmental dimensions and risk management in design.
- Analyse the performance of digital and analog communication, mobile communication, coding, and decoding systems.

### **Subject-Based Practical skills**

- Apply knowledge of mathematics, science, information technology, design, business context and engineering practice integrally to solve communication systems problems.
- Use computational facilities and techniques, measuring instruments, workshops and laboratory equipment to design experiments, collect, analyse and interpret results.
- Use a wide range of analytical tools, techniques, equipment, and software packages pertaining to the discipline and develop required computer programs.
- Apply quality assurance procedures and follow communication systems codes and standards.
- Exchange knowledge and skills with communication systems engineering community and industry.
- Apply computer programming for the design and diagnostics of digital and analogue communication, mobile communication, coding, and decoding systems.

### **Skills for life and work (general skills)**

- Collaborate and communicate effectively within multidisciplinary team.
- Work in stressful environment and within constraints.
- Effectively manage tasks, time, and resources.
- Search for information and engage in life-long self-learning discipline.

## **2.4 Professional body accreditation**

The National Academic Reference Standards (NARS) for Engineering set out generic statements which represent general expectations about standards for the Bachelor of Engineering. These statements clarify the attributes associated with the award of engineering degrees:

- The awards are in accord with the frameworks for contemporary engineering education.
- The Engineering degrees address the national expectations of the graduate engineers.
- The degrees satisfy the actual and expected market needs.

According to the Accreditation Board for Engineering and Technology (ABET), Engineering is the knowledge of the mathematical and natural sciences, gained by study, experience, and practice, applied with judgment to develop ways to economically utilise the materials and forces of nature for the benefit of mankind. It is the ability to initiate and conduct activities associated with engineering processes, systems, problems, opportunities, history, future impacts and ethics with minimal negative consequences. It involves knowledge, ways of thinking, action coordination and capability development. It helps preparing individuals to make well-informed choices whether they act as consumers, workers, citizens or members of the global community.

The engineering education should achieve excellence in undergraduate and graduate education, research, public service and advancement of the state-of-the art within the discipline. It aims to produce able, broadly educated, highly qualified engineers through academic excellence. Moreover, it motivates students, faculty and staff to learn, grow, achieve and serve the needs of society nationally, regionally and internationally. It also prepares students for a productive and rewarding career in engineering based on strong moral and ethical foundation.

The references for standards considered in the development of this programme were the Academic Reference Standards (ARS) of Communication System Engineering Programme, August 2009 (as a minimum standards) prepared by the Electronics and communication engineering education sector of the supreme council of universities in Egypt. Available copies of the NARS (issued in August 2009).

## **2.5 Programme Structure & Content**

The BEng (Hons) Communication Systems Engineering degree is a four-year UEL/ASU double award programme, i.e. levels 3–6. The programme conforms to UEL's Academic Framework structure. The majority of modules are 30 credits delivered over the whole year and suit a September in take only. A few modules are 15 credits and delivered sequentially to meet specific professional learning outcomes. The student chooses one of the three tracks of specialization as a major track.

All modules will be taught/delivered and assessed in English. Each module will have a named Module Leader from ASU. The programme Leader, who has overall responsibility for the day-to-day running of the programme is Assoc. Prof. Dr.



Mohamed Abouelatta. Students will pay all tuition/study/workshop/course field trip fees directly to ASU. Details of the programme structure can be seen in Table 2.5.

### **Intermediate Awards**

If students are unable to complete their studies, the following awards can be made:  
In order to gain a BEng unclassified degree (ordinary degree) students will need to obtain a minimum of 300 credits including:

- A minimum of 120 credits at level four or higher
- A minimum of 120 credits at level five or higher
- A minimum of 60 credits at level six or higher

In order to gain a Diploma of Higher Education students will need to obtain at least 240 credits including a minimum of 120 credits at level four or higher and 120 credits at level five or higher.

In order to gain a Certificate of Higher Education students will need to obtain 120 credits at level four or higher.

In order to gain an Undergraduate Certificate students will need to obtain 40 credits at level three or higher.

### **Design of the Programme**

The design and content of the Communication Systems Engineering programme has been determined by a number of considerations including:

- to meet the national Benchmark Standards and the requirements of the National Framework for Higher Education Qualifications (see [www.qaa.ac.uk](http://www.qaa.ac.uk) for details).
- To meet the UEL Academic Framework Modular Regulations and other university policies ([www.uel.ac.uk/academicframework](http://www.uel.ac.uk/academicframework)).
- To reflect the research and professional interests of the staff. The options on offer are taught by staff who is specialists in those areas. In this way, you will be exposed to up to date research and also gain awareness of professional practice.
- To build up your knowledge and extend your skills as you go through the years. Each Year/Level of the programme draws on and expands material presented at earlier stages. You will be expected to tackle more specialist topics and in more breadth and depth, to develop more critical evaluation and analysis of material, to begin to integrate material across modules, to rely less on basic text books and to read more original material, and to work more independently, with less guidance.

- To offer opportunities for you to develop career and work related skills. Certain modules are specifically designed to help you with this but all modules offer opportunities for practice and development.

Table 2.5 BEng Communication Systems Programme Structure.

Note: The listed modules' codes are temporary, and they will be updated later according to the UEL partner Web Marks Entry (WME) system

Level	Year	Code	Module title	credit	Core/ Track Related
<b>Communication Systems Engineering</b>					
3	1	EG10311	Applied Mathematics for Communications Engineering	30	Core
3	1	EG10312	Foundations of Physics	30	Core
3	1	EG10313	Engineering and Electronic Materials	30	Core
3	1	EG10314	Circuits and Fields theory	30	Core
<b>Communication Systems Engineering</b>					
4	2	EG10421	Digital Design and Computer architecture	30	Core
4	2	EG10422	Electronic Devices & Circuits	30	Core
4	2	EG10423	Fundamentals of Microwave Engineering	30	Core
4	2	EG10424	Fundamentals of signals & Communications	30	Core
<b>Communication Systems Engineering</b>					
5	3	EG10531	Analog and Digital Circuits	30	Core
5	3	EG10532	Antenna Engineering	15	Core
5	3	EG10533	Digital Transmission and Networks	30	Core
5	3	EG10534	Control Engineering	15	Core
<b>Track 1: Signals and Communication Systems</b>					
5	3	EG10535	Satellite Communications	15	Elective
5	3	EG10536	Statistical Signal Processing	15	Elective
<b>Track 2: Circuits and Systems</b>					
5	3	EG10537	VLSI Technology	15	Elective
5	3	EG10538	Introduction to Embedded Systems	15	Elective
<b>Track 3: Physical and Wave Electronics</b>					
5	3	EG10539	Microwave Circuits	15	Elective
5	3	EG105310	Optical Sensing	15	Elective
<b>Communication Systems Engineering</b>					
6	4	EG10641	Advanced Communication Networks	15	Core
6	4	EG10642	Graduation Project	30	Core
6	4	EG10643	Entrepreneurship	15	Core
6	4	EG10644	Information Theory and Coding	15	Core
<b>Track 1: Signals and Communication Systems</b>					
6	4	EG10645	Wireless and Mobile Communications	30	Elective

6	4	EG10646	Processing for Multimedia	15	Elective
Track 2: Circuits and Systems					
6	4	EG10647	Analog and RF Circuit design	30	Elective
6	4	EG10648	ASIC Design & Automation	15	Elective
Track 3: Physical and Wave Electronics					
6	4	EG10649	Microwave Systems	30	Elective
6	4	EG106410	Optical Communication	15	Elective

FoE-ASU modify the programmes Bylaws every five years to cope with the advances in engineering technologies and/or enforcing corrective actions to face any deficiencies in the previous Bylaws. The current enrolled students on the Communication Systems Engineering programme are registered on the 2013 Bylaw, while the students who will register in the academic year 2019-2020 will be enrolled at 2018 Bylaw. For students who are enrolled on the 2013 Bylaw and want to complete the BEng (Hons) from UEL, an equivalency will be made for the courses achieved by student(s) to determine which level s/he will be enrolled at. As for students who will be enrolled on the 2018 Bylaw and want to complete the BEng (Hons) from UEL, they will register the corresponding UEL level modules and follow either full time or part time study modes.

The following table shows the content of each module of the COMM programme (Bylaw 2013 and Bylaw 2018), percentage weighting and the assessment method:

Module Code	Module Name	Component of Assessment	Percentage Weighting	Component of Assessment	Percentage Weighting	Assessment Method
		2013 Bylaws		2018 Bylaws		
Communication Systems Engineering (UEL) Foundation (Level 3) - ASU Year 1						
EG10311 30 Credits	Applied Mathematics for Communications Engineering	PHM113 Calculus for Engineering (3)	25%	PHM111 Probability and Statistics	25%	Four portfolios of students' work include coursework of the four ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"><li>• Participation</li><li>• Assignments</li><li>• Quizzes</li><li>• A midterm exam</li><li>• Final Exam</li></ul>
		PHM115 Differential Equations and Partial Differential Equations	25%	PHM113 Differential Equations and Partial Differential Equations	25%	
		PHM114 Statistics and Probability for Engineering	25%	PHM114 Numerical Analysis	25%	
		PHM116 Complex and special functions and Fourier Analysis	25%	PHM213 Complex and special functions and Fourier Analysis	25%	
EG10312 30 Credits	Foundations of Physics	PHM221: Optical and Thermal Physics	50%	PHM123 Thermal and Statistical Physics	50%	Two portfolios of students' work include coursework of the two ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"><li>• Participation</li><li>• Practical demonstrations</li><li>• Assignments</li><li>• Quizzes</li><li>• A midterm exam</li><li>• Final Exam</li></ul>
		PHM123: Modern Physics and Quantum Mechanics	50%	PHM121: Modern Physics and Quantum Mechanics	50%	
EG10313 30 Credits	Engineering and Electronic Materials	MDP132 Structures and Properties of Materials	50%	EPM211 Properties of Electrical Materials	50%	Two portfolios of students' work include coursework of the two ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"><li>• Participation</li><li>• Practical demonstrations</li><li>• TCAD Simulation Report</li><li>• Assignments</li><li>• Quizzes</li><li>• A midterm exam</li></ul>
		ECE132 Electronic Materials	50%	ECE111 Electronic Materials	50%	

						<ul style="list-style-type: none"><li>Final Exam</li></ul>
EG10314 30 Credits	Circuits and Fields theory	<b>EPM114</b> Electrical Circuits	50%	<b>EPM114</b> Fundamentals of Electrical Circuits	50%	Two portfolios of students' work include coursework of the two ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"><li>Participation</li><li>Reports &amp; Assignments</li><li>Quizzes</li><li>A midterm exam</li><li>Final Exam</li></ul>
		<b>ECE161</b> Electrostatics and Electromagnetics	50%	<b>ECE131</b> Electrostatics and Electromagnetics	50%	
Communication Systems Engineering (UEL) Level 4 – (ASU) Year 2						
EG10421 30 Credits	Digital Design and Computer architecture	<b>CSE122</b> Computer programming	30%	<b>CSE131</b> Computer programming	30%	Three Portfolios of students' work include coursework of the three ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"><li>Participation</li><li>Practical demonstrations</li><li>Assignments</li><li>Quizzes</li><li>A midterm exam</li><li>Final Exam</li></ul>
		<b>CSE141</b> Logic Design	35%	<b>CSE111</b> Logic Design	35%	
		<b>CSE212</b> Computer Architecture	35%	<b>CSE212</b> Computer Organization	35%	
EG10422 30 Credits	Electronic Devices & Circuits	<b>ECE233</b> Solid State Electronic Devices	50%	<b>ECE213</b> Solid State Electronic Devices	50%	Two portfolios of students' work include coursework of the two ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"><li>Participation</li><li>Practical demonstrations</li><li>Assignments</li><li>TCAD Simulation Report</li><li>Quizzes</li><li>A midterm exam</li><li>Final Exam</li></ul>
		<b>ECE242</b> Electronic Circuits (1)	50%	<b>ECE214</b> Electronic Circuits (1)	50%	
EG10423 30 Credits	Fundamental s of Microwave Engineering	<b>ECE261</b> Engineering Electromagnetics	50%	<b>ECE331</b> Electromagnetic Waves	50%	Two portfolios of students' work include coursework of the two ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"><li>Participation</li><li>Practical demonstrations</li><li>Assignments</li><li>Quizzes</li><li>A midterm exam</li><li>Final Exam</li></ul>
		<b>ECE262</b> Waves and Transmission Lines	50%	<b>ECE333</b> Microwave Engineering	50%	
EG10424 30 Credits	Fundamental s of signals & Communicati ons	<b>ECE 252</b> Signals and Systems	35%	<b>ECE 253</b> Signals and Systems	35%	Three portfolios of students' work include coursework of the three ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"><li>Participation</li><li>Practical demonstrations</li></ul>
		<b>ECE253</b> Analog Communication Systems	35%	<b>ECE254</b> Analog Communications	35%	

		<b>ECE254</b> Digital Signal Processing	30%	<b>ECE255</b> Digital Signal Processing	30%	<ul style="list-style-type: none"> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
<b>Communication Systems Engineering (UEL) Level 5 – (ASU) Year 3</b>						
EG10531 30 Credits	Analog and Digital Circuits	<b>ECE343</b> Electronic Circuits (2)	50%	<b>ECE315</b> Electronic Circuits (2)	50%	Two portfolios of students' work include coursework of the two ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Assignments</li> <li>• Project</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
		<b>ECE344</b> Digital Circuit Design	50%	<b>ECE316</b> Digital Circuit Design	50%	
EG10532 15 Credits	Antenna Engineering	<b>ECE363</b> Antenna Engineering and Propagation	100%	<b>ECE432</b> Antenna Engineering and Propagation	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab Reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
EG10533 30 Credits	Digital Transmission and Networks	<b>ECE354</b> Digital Communications	50%	<b>ECE354</b> Digital Communications	50%	Two portfolios of students' work include coursework of the two ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Reports</li> <li>• Projects</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
		<b>ECE355</b> Communication Networks	50%	<b>ECE355</b> Communication Networks (1)	50%	
EG10534 15 Credits	Control Engineering	<b>CSE373</b> Control Systems	100%	<b>CSE371</b> Control Engineering	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
<b>Track 1: Signals and Communication Systems</b>						
EG10535 15 Credits	Satellite Communications	<b>ECE358</b> Satellite Communications	100%	<b>ECE454</b> Satellite Communication Systems	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Research Reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>

EG10536 15 Credits	Statistical Signal Processing	<b>ECE359</b> Statistical Signal Processing	100%	<b>ECE357</b> Statistical Signal Processing	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
<b>Track 2: Circuits and Systems</b>						
EG10537 15 Credits	VLSI Technology	<b>ECE381</b> VLSI Technology	100%	<b>ECE411</b> Integrated Circuits Technology	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Research Reports</li> <li>• Project</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
EG10538 15 Credits	Introduction to Embedded Systems	<b>CSE341</b> Introduction to Embedded Systems	100%	<b>CSE211</b> Introduction to Embedded Systems	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Project</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
<b>Track 3: Physical and Wave Electronics</b>						
EG10539 15 Credits	Microwave Circuits	<b>ECE364</b> Microwave Circuits	100%	<b>ECE337</b> Microwave Circuits	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
EG105310 15 Credits	Optical Sensing	<b>ECE493</b> Selected Topics in Physical and Wave Electronics	100%	<b>ECE338</b> Optical Sensing and Instrumentation	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab reports</li> <li>• practical demonstrations</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
<b>Communication Systems Engineering (UEL) Level 6 – (ASU) Level 4</b>						
EG10641 15 Credits	Advanced Communicati on Networks	<b>CSE435</b> Computer Networks	100%	<b>ECE458</b> Communication Networks (2)	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab reports</li> </ul>



						<ul style="list-style-type: none"> <li>Fieldwork reports</li> <li>Assignments</li> <li>Quizzes</li> <li>A midterm exam</li> <li>Final Exam</li> </ul>
EG10642 30 Credits	Graduation Project	<b>ECE497</b> Graduation Project (1)	50%	<b>ECE491</b> Graduation Project (1)	50%	Two portfolios of students' work include coursework of the two ASU- modules; each portfolio includes 8000-word project report, intermediate deliverables and a 15-minute viva presentation
		<b>ECE498</b> Graduation Project (2)	50%	<b>ECE492</b> Graduation Project (2)	50%	
EG10643 15 Credits	Entrepreneurship	<b>ECE496</b> High-Tech Entrepreneurship	100%	<b>ASU321</b> Innovation and Entrepreneurship	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>Participation</li> <li>Project</li> <li>Fieldwork reports</li> <li>Oral presentations</li> <li>Quizzes</li> <li>A midterm exam</li> <li>Final Exam</li> </ul>
EG10644 15 Credits	Information Theory and Coding	<b>ECE458</b> Information Theory and Coding	100%	<b>ECE462</b> Information Theory and Coding	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>Participation</li> <li>Lab reports</li> <li>Assignments</li> <li>Quizzes</li> <li>A midterm exam</li> <li>Final Exam</li> </ul>
<b>Track 1: Signals and Communication Systems</b>						
EG10645 30 Credits	Wireless and Mobile Communications	<b>ECE459</b> Wireless and Mobile Communications	100%	<b>ECE358</b> Wireless Communications	50%	Two portfolios of students' work include coursework of the two ASU- modules in bylaw 2018 (One portfolio only in bylaw 2013); each portfolio includes <ul style="list-style-type: none"> <li>Participation</li> <li>Fieldwork reports</li> <li>Assignments</li> <li>Quizzes</li> <li>A midterm exam</li> <li>Final Exam</li> </ul>
				<b>ECE459</b> Mobile Communications	50%	
EG10646 15 Credits	Processing for Multimedia	<b>CSE367</b> Digital Image Processing	100%	<b>ECE359</b> Signal Processing for Multimedia	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>Participation</li> <li>Oral presentations</li> <li>Fieldwork reports</li> <li>Assignments</li> <li>Quizzes</li> <li>A midterm exam</li> <li>Final Exam</li> </ul>
<b>Track 2: Circuits and Systems</b>						

EG10647 30 Credits	Analog and RF Circuit design	<b>ECE382</b> Analog Integrated Circuits Design	50%	<b>ECE412</b> Analog Integrated Circuit Design	50%	Two portfolios of students' work include coursework of the two ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Fieldwork reports</li> <li>• Project</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
		<b>ECE488</b> RF Circuit Design	50%	<b>ECE414</b> RF Circuit Design	50%	
EG10648 15 Credits	ASIC Design & Automation	<b>ECE487</b> VLSI Design & Automation	100%	<b>ECE413</b> ASIC Design & Automation	100%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• CAD reports</li> <li>• Oral presentations</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
<b>Track 3: Physical and Wave Electronics</b>						
EG10649 30 Credits	Microwave Systems	<b>ECE463</b> Microwave Devices	50%	<b>ECE438</b> Microwave Devices	50%	Two portfolios of students' work include coursework of the two ASU- modules; each portfolio includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Fieldwork reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>
		<b>ECE464</b> Microwave Measurements	50%	<b>ECE335</b> Microwave Measurements	50%	
EG106410 15 Credits	Optical Communicati on	<b>ECE356</b> Optical Communication Systems	35%	<b>ECE334</b> Optical Fiber Communications	35%	Portfolio of students' work includes samples of the following: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab reports</li> <li>• Practical demonstrations</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>

### 3 KEY STAFF AND CONTACT DETAILS

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***COMM.CHEP@eng.asu.edu.eg***

## **Programme Organisation**

The organisation and administration of the programme will be carried out through the following:

### **The Dean of Faculty of Engineering**

Prof. M. Ayman Ashour is the Dean of Faculty of Engineering at ASU. He has overall responsibility for maintaining the high standards of quality and innovation in all our teaching and research activities.

### **The Programme Leader**

Assoc. Prof. Mohamed Abouelatta is the programme leader for the BEng (Hons) Communication Systems Engineering programme. The programme leader represents the academic interests of the programme, coordinates the day-to-day business of programme, and has overall responsibility for students on the programme. The role of the programme leader is to guide each student registered on the programme through the duration of the programme and is the first port of contact when programme level issues occur. The programme leader, in conjunction with the academic support team, is responsible with the day-to-day running of the programme. The programme leader is there to resolve any issues that may arise at the programme level and will mediate between module leaders & the academic support team to resolve any programme level issues. If you have a problem with a particular module and have not been able to resolve it by talking to the Module Leader, you should bring the matter to the Programme Leader. Programme Leader is also responsible for liaison with Programme Representatives for the year. He also has other duties, which vary from year-to-year and are often connected with quality improvement projects.

### **The Programme Management Team**

The Programme Management Team consists of the Programme Leader, Module Leaders, School Administrators and the Student Representatives, are collectively responsible for day-to-day running of the programme. We have Programme Committees and Meetings to discuss any issues that arise throughout the academic teaching and/or other subjects and these happen at least one per term.

### **The Module Leaders**

Your Module Leaders are responsible for delivery and academic management of the module, including all module assessment tasks. The module leader is responsible for the delivery of an individual module and is tasked with providing the students with the necessary lecture and tutorial material and assessing the work submitted. They will take all of the lectures for their module. As far as possible any problems or questions concerning individual modules should be addressed to the Module Leader. In most cases this can be done within seminars, workshops or practical sessions. General academic advice can also be obtained from them.

## **External Examiners**

External Examiners are responsible for providing an independent check that proper standards are being maintained and are allocated to modules by Subject Area. They review each piece of assessment before it is available to students, review samples of work each semester, and review student feedback and results.

## **Circumstances in which student can access UEL directly**

You will find that for most issues that arise during the course of your studies, academic and administrative staff at your location of study will be able to help, and further details are provided in this handbook. If however you have concerns that lie outside the remit of these staff you can contact the UEL link person [see further details below] in the first instance who will be able to re-direct your enquiry as appropriate.

The UEL Academic Link Tutor is appointed to manage the relationship between the Programme Leader at ASU- FoE and UEL. Students may meet the UEL Link Person at Programme Committee Meetings.

Please contact your local Student Support/Administrative Office if you have any queries, in the first instance. If you have been advised by your local office to contact UEL then please send an e-mail to the **UEL Academic Link Tutor** at ***[h.hakimazari@uel.ac.uk](mailto:h.hakimazari@uel.ac.uk)***.

## 4 PROGRAMME OPERATION AND STUDENT REGISTRATION

### 4.1 Study Timings and Registration

The academic year comprises three semesters:

- **First main semester (Fall):** Begins early September and lasts for 15 weeks.
- **Second main semester (Spring):** Begins early February and lasts for 15 weeks.
- New students' enrolment in the programmes starts two weeks before the starting of the Fall semester, after fulfilling all the programmes requirements and paying the enrolment fees, as recommend by the Programs Administration Council and set by the Council of the Faculty of Engineering.
- Registration for any semester takes place within two weeks before the starting day of the semester. Registration is not final until the full tuition fees of the semester are paid.
- Registration in the Summer semester is optional.
- The student must register 120 credits per academic year or 60 credits per semester, after consulting the academic advisor, at the time of registration and according to the yearly rules issued by the Faculty and published in the student's guide. Registration is not final until the student pays the educational service fees for the semester.
- There will be one intake point per year, which will be in September.
- Late registration is not final unless there is a vacancy in the courses, and the student should pay late registration fees besides the prescribed academic service fees, in accordance with the recommendations of the Programmes Administration Council and approval of the Council of the Faculty of Engineering regarding this issue.
- The student may not register in any course without fulfilling all its prerequisites.
- The programme academic regulations are available at ***<https://eng.asu.edu.eg/BylawsAndRegulations>***
- The Local Attendance and Engagement policy is available at ***[https://eng.asu.edu.eg/uploads/uploadcenter/asu\\_594\\_file.pdf](https://eng.asu.edu.eg/uploads/uploadcenter/asu_594_file.pdf)***
- UEL University's academic regulations are available at: Academic Framework Regulations (see Manual of General Regulations, Part 3)
- ***<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Manual-of-General-Regulations>***

It is essential that you log in to UEL direct and enrol with UEL using the UEL student number that you have be given prior to attending any lectures. Your programme leader will be able to assist you with this.

Once you have gained admission to the programme you must login to the UEL direct page using your student username which will be your UEL ID number and password and complete the on-line enrolment. ASU – FoE will assist and

ensure that you complete your online enrolment task promptly. UEL Direct is available at ***<https://www.uel.ac.uk/students>***

For general enquiries concerning enrolment, you must contact your local Student Support/Administrative Office for guidance in the first instance and then if you are advised to contact UEL, please send an e-mail to the UEL Academic and Employers Partnerships Office at ***[h.hakimazari@uel.ac.uk](mailto:h.hakimazari@uel.ac.uk)***.

## **5 TEACHING, LEARNING AND ASSESSMENT**

### **5.1 Details of local teaching and learning approaches**

- No distance learning is implemented.
- The programme is a credit bearing system leading to the Bachelor of Science Degree (Honours) after completing 480 credits. Student evaluation is based not only on final exam, but also on midterm exams, quizzes, assignments, course projects, presentations, papers, essays, in/out of class participation and many other innovative activities.
- The student must pass the College requirements, which consist of basic sciences and engineering courses. These courses must be studied by all students and they represent 120 credits at Foundation level.
- Course instructors in the programme are carefully selected from the distinct full-time world-class faculty members of the Faculty of Engineering at Ain Shams University.
- Although this is not compulsory, the student is encouraged to perform summer training for 12 weeks during study duration. Training must be performed in an industrial/service facility related to the student's programme and must be under the full supervision of the faculty.
- With the majority of modules being delivered over the whole year there is excellent scope for formative assessment to stretch and extend the students. Thus, a key feature of the courses is the emphasis on formative feedback and guidance to enable students to develop full understanding of the topics of study, prior to assessment taking place.
- Assessment for these programmes takes the form of examinations, course works, presentations and time constrained assessments as can be seen below in the Assessment Workload mapping.
- Each course syllabus should contain: course objectives, textbook, outline, material, assessments, grading policy and outcome. Outline should contain sections covered every week with reference to chapters/sections in the textbook. The instructor should give the course syllabus to the students in the first class. The syllabus serves as a contract between the instructor and the students.

The following are not compulsory for the double award programmes but will be encouraged:

- The student should pass the ASU's requirements, which consist of humanities, social sciences, general culture courses. These courses represent 18 credit hours at ASU selected from a list of courses.
- The student must pass the ASU's College requirements, which consist of basic sciences and engineering courses. These courses must be studied by all students.
- The student must perform summer training for 12 weeks during their study duration and should be conducted during 3 summers. Training must be performed in an industrial/service facility related to the student's program or inside the faculty where it is delivered by staff members. The training must be under the full supervision of the faculty. The student submits their training



portfolio to their Academic Advisor, who in turn assesses the outcomes and evaluates it.

## **ASU Attendance Policy**

Across the faculty, consistent attendance of at least 75% and participation in programme activities is part of the learning process. To meet all learning outcomes, FoE- ASU expects full attendance in all lectures and insufficient attendance may result in an 'Incomplete' status for the course. The school should be notified of absences. In case of illness a recognized medical certificate should be supplied. Students are encouraged to communicate with their lecturer or course coordinator if they have any queries.

## **Assessment**

The module specifications provide a detailed breakdown of the weighting and volume of assessment: these can be found in the Table above. For a formal description of the assessment process students should refer to the Academic Regulations on the UEL website or refer to details in the guide for students. FoE - ASU has a broad experience in providing formative and summative assessment, thus migrating to the UEL Framework will not be a major issue.

## **Assessment Arrangements**

Each module assessment will be designed and set in accordance with the module specification. This will state the number of components to be assessed as well as the weighting of each component. Each assessment will be moderated/verified internally at ASU before it is sent to UEL for approval. All module or component assessments must be formally approved before they are issued to students. All assessments will be approved via the normal and established UEL procedure(s). A marking criteria will be published to students using either a rubric or more detailed written explanation and will be provided to students at the same time as the assessment specification/task. This will form part of the assessment brief which will be agreed with the external examiner.

Marking of assessments will use the full scope of marks, that is 0 – 100. A sample of 10% or 10 scripts (whichever is greater) must be second marked by ASU and this must cover the full range of marks. In the case of the research project (or similar work), the work of the entire cohort will be blind double-marked. The samples (including both second marked and non-second marked) will be sent to UEL for forwarding to the External Examiner for review.

UEL will determine what documents/information is needed for an Assessment Board and this will be communicated to ASU in a timely manner.

All summative assignments will be marked anonymously where possible and subject to second marking. ASU will conduct a pre-board where all modules and profiles of students will be considered, and this will be fed back to UEL who will consider these at the relevant UEL Assessment Board. The results will be considered at assessment boards, which will be held at UEL. Feedback will be given to all students especially on

summative assessment tasks. Normally the module leader will choose how this is given, but generally it will be given individually (within 20 days).

UEL operates a minimum of 30% threshold in each component of assessment on a module. However, to pass the module students will need to achieve a weighted average of at least 40%. Progression to the next higher level (year) will only be permitted if the student has gained at least 90 credits during the academic year.

On the UEL/ASU double programme, students will not be permitted to study any level six (6) modules, if there are outstanding level four (4) modules. The Assessment Board at UEL (with representation by the Academic Link Tutor) will determine the progression decision of all students.

### **ASU Assessments vs UEL/ASU Double Assessment Arrangements**

On the UEL/ASU double award programme, students must pass the agreed UEL module in conformity with all established rules and procedures as determined by UEL. If a student has failed a module or component of a module on the UEL/ASU double award programme, the student will be entitled to a resit opportunity. This will normally be in the early summer (July/August).

Students will be asked and expected to retake a module with attendance if a resit opportunity was not successfully passed; however, this depends on the individual profile of the student – taking into consideration UEL policy/rules on retakes.

Although reassessment on modules is not permitted on the Egyptian award, yet students are able to review the course work grades and discuss with the instructor the marking of the course work. As for the final exam students are permitted to submit an appeal for revising the marks registration and the completeness of grading the final exam paper. However, modules reassessment is possible on the UEL award according to UEL regulations.

UEL's "capping" regulations will apply for any resit or retake modules or components of modules. Passing an ASU module or component of a module does not automatically mean that the UEL/ASU double award module has been passed. There will be no averaging (mean) of module marks on ASU modules to determine UEL/ASU double award module marks. The marks of a module will be as specified on the module specification.

If a student fails a module on the ASU variant of the programme but passes the UEL/ASU double award module: This student would have been deemed to pass the module and would be given the credits for such module.

An agreed equivalence chart/table will be used to compare ASU marking/grading scheme to that of the UEL/ASU double programme. However, in all cases, on the UEL/ASU double award programme the full spectrum of marks (0-100) will be used.

Students will be entitled to UEL's "compensated pass" regulations on the double award programme. Summer training/placements/work is not a formal part of the UEL/ASU double programme but will be encouraged.

## **Moderation of Assessment**

Examinations and other assessments undergo a rigorous quality assurance process of moderation as follows:

### **Preparing the assessment brief / examination paper**

- Module lecturers design/ write the questions / briefs and produce answers with marking schemes.
- Another lecturer checks the assessment questions, solutions and marking scheme.
- Copies of the assessment questions, answers and marking scheme are sent to UEL for checking and approval.
- UEL sends the assessments to external examiners for approval.

### **Marking of assessments**

- Students' assessments are marked by the FoE- ASU teaching staff.
- A sample of 10% or 10 scripts, whichever is the higher, are double marked by another lecturer within FoE-ASU
- In the case of exam scripts, the papers of the entire cohort is blind double-marked
- The double marked sample is sent to UEL for forwarding to the External Examiner
- The results are considered at assessment boards.

All summative assignments are marked anonymously where possible and subject to second marking. If they can't be marked anonymously, the assignments will be double-marked. The ASU examination board will conduct a pre-board where all modules and profiles of students will be considered. This will be fed back to UEL who will consider these at the relevant UEL Assessment Board.

## **Submission of Coursework**

ASU has its own mechanisms and procedure for coursework submission and these will apply. Students will be informed of this procedure during induction. ASU is committed to facilitating Turnitin submission within 12-18 months and advice will be sought from the School of ACE at UEL as to how to implement this. The module handbook/guidelines will explicitly detail how coursework should be submitted and these will (using student number, word count, word-processed). Submission dates will be available in the Module Guides and on the VLE. Work which is submitted late, but within 24 hours of the deadline, will be assessed but subjected to a fixed penalty of 5% of the total marks available (as opposed to marks obtained).

Please note that if you submit twice, once before the deadline and once during the 24 hour late period, then the second submission will be marked and 5% deducted.

Further information is available in the Assessment & Feedback Policy at [\*\*https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Assessment-and-Feedback-Policy\*\*](https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Assessment-and-Feedback-Policy)

## Extenuating circumstances claims

Under certain circumstances, extenuation can be granted. Academic staff should direct students to FoE ASU support staff trained on UEL extenuation processes as outlined in UEL's extenuation policy as FoE – ASU will follow the process of UEL for the Extenuating circumstances:

**<https://www.uel.ac.uk/discover/governance/policies-regulations-corporate-documents/student-policies/extenuation-procedures>**

Normal UEL criteria will apply. A subcommittee will be set up at FoE - ASU under the guidance of the Academic Link Tutor. This committee will report its finding and determination to UEL (APO and ALT).

## Breaches of Academic Misconduct Regulations

Assessment tasks are designed to reduce, as far as is practicable, the possibility of plagiarism and collusion and other instances of academic misconduct. Where an instance of academic misconduct is suspected, procedures detailed in Part 8 of Manual of General Regulations (Academic Misconduct Regulations of UEL) will be invoked. The cases will be identified through Turnitin facilities provided by UEL for the registered students and they will be dealt with the same procedures mentioned in the General Regulations manual. Students will be made aware of the Academic Integrity Policy to assist in the avoidance of plagiarism. As part of their induction, students will also be required to complete the academic integrity certificate on Moodle.

The following is a non-exhaustive list of examples of academic misconduct:

**Plagiarism:** representing another person's work or ideas as one's own, for example by failing to follow convention in acknowledging sources, use of quotation marks etc. This includes the unauthorised use of one student's work by another student and the commissioning, purchase and submission of a piece of work, in part or whole, as the student's own.

**Collusion:** cooperation in order to gain an unpermitted advantage. This may occur where students have consciously collaborated on a piece of work, in part or whole, and passed it off as their own individual efforts or where one student has authorised another to use their work, in part or whole, and to submit it as their own.

**Misconduct in examinations** (including in-class tests). Including, for example, when an examination candidate:

- copies from the examination script of another candidate;
- obtains or offers any other improper assistance from or to another candidate (or any other person unless an approved reader or scribe);
- has with them any unauthorised book (including mathematical tables), manuscript or loose papers of any kind, unauthorised electronic devices (including mobile telephones) or any source of unauthorised.
- allows himself/herself to be impersonated or when any person impersonates another examination candidate.

Further information of UEL's academic misconduct policy is available at:

***[https://www.uel.ac.uk/-/media/main/images/about/temp\\_governance\\_prototype/policies-and-regulations/students/manual-of-general-regulation-2017/part8-academic-misconduct-regulations-sept-2017-final](https://www.uel.ac.uk/-/media/main/images/about/temp_governance_prototype/policies-and-regulations/students/manual-of-general-regulation-2017/part8-academic-misconduct-regulations-sept-2017-final)***

**Fabrication or misrepresentation:** the presentation of fabricated data, results, references, evidence or other material or misrepresentation of the same. Including, for example:

- claiming to have carried out experiments, observations, interviews or other forms of research which a student has not, in fact, carried out;
- claiming to have obtained results or other evidence which have not, in fact, been obtained;
- in the case of professional qualifications, falsely claiming to have completed hours in practice or to have achieved required competencies when this is not the case;

**Failure to obtain ethical approval:** where work is undertaken without obtaining ethical approval when there is a clear and unambiguous requirement to do so. FoE ASU will use a range of mechanisms for determining academic misconduct including and not limited to, plagiarism software, internet searches, viva voce.

## **Feedback to Students**

Feedback will be given to all students especially on summative assessment tasks. Normally the module leader will choose how this is given, but generally it is given individually. Assessment feedback is provided to you so that you can use the feedback to improve your future performance. You will be also provided with feedback on formative tasks – these are tasks that do not lead to a final mark or grade. The lecturer or the module leader will determine how this is given.

Feedback is central to learning and is provided to you to develop your knowledge, understanding, skills and to help promote learning and facilitate improvement.

All feedback will be:

- timely (provided within 20 working days)
- given in relation to the learning outcomes and assessment criteria
- provided on both coursework and examinations
- clear, relevant, motivating, and constructive
- developmental, enabling you to consolidate learning and achievement
- word-processed where e-submission is not used (unless the nature of the work prevents this e.g. mathematical formula)
- offered in a range of formats appropriate to the module e.g. electronically via Turnitin Grade Mark or other e-Submission tools where used, Audio file, Video file, or Screen cast.

## **Assessment Boards**

Assessment Boards control, consider and adjudicate upon all assessments undertaken by students. The Board comprises a Chair (usually a Head of Department),

all those substantially involved such as lecturers/tutors/module leaders and the external examiner(s).

### **Mapping of assessment schedule to UEL Boards**

Submission dates will be planned in collaboration with the UEL Academic Link Tutor to ensure that the marking process is complete, and marks are entered in time for the appropriate board at UEL.

### **Use of Virtual Learning Environment (VLE) in the learning and assessment process;**

Currently, the ASU uses a VLE where module content material such as lecture slides, tutorial and practical tasks are uploaded for the students to access.

## **5.2 Details of local assessment arrangements**

The students work is submitted for each individual ASU course in the form and deadline instructed via ASU assignment and goes via the normal marking process. Further the student work for each individual ASU courses is packed in a portfolio format for the submission requirement for the UEL degree.

We strongly suggest that you try to submit all coursework by the deadline set as meeting deadlines is expected in employment. However, in our regulations, UEL has permitted students to be able to submit their coursework up to 24 hours after the deadline. The deadline will be published in your module guide. Coursework which is submitted late, but within 24 hours of the deadline, will be assessed but subjected to a fixed penalty of 5% of the total marks available (as opposed to marks obtained).

Please note that if you submit twice, once before the deadline and once during the 24 hour late period, then the second submission will be marked and 5% deducted.

Further information is available in the Assessment & Feedback Policy at <https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Assessment-and-Feedback-Policy>.

### **Grades of the COMM Programme modules**

The points of each credit hour are computed as follows:

<b>Ain Shams University</b>			<b>University of East London</b>
<b>Percentage of total mark at ASU</b>	<b>Grade</b>	<b>Points for GPA</b>	<b>Percentage equivalent at UEL</b>
97% and higher	A+	4.0	95% and higher
93% to less than 97%	A	4.0	82% to less than 95%
89% to less than 93%	A-	3.7	70% to less than 82%
84% to less than 89%	B+	3.3	66% to less than 70%
80% to less than 84%	B	3.0	63% to less than 66%

76% to less than 80%	B-	2.7	60% to less than 63%
73% to less than 76%	C+	2.3	56% to less than 60%
70% to less than 73%	C	2.0	53% to less than 56%
67% to less than 70%	C-	1.7	50% to less than 53%
64% to less than 67%	D+	1.3	45% to less than 50%
60% to less than 64%	D	1.0	40% to less than 45%
Less than 60%	F	0.0	Less than 40%

### 5.3 References to student policies

ASU-FoE available at:

**[https://eng.asu.edu.eg/uploads/uploadcenter/asu\\_594\\_file.pdf](https://eng.asu.edu.eg/uploads/uploadcenter/asu_594_file.pdf)**

UEL available at:

**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies>**

Also detailed in Appendix B which provides full information on referencing and the avoidance of plagiarism.

The electronic version of “Cite Them Right: *the essential referencing guide*” 9th edition, can be accessed whilst on or off campus, via UEL Direct. The book can only be read online and no part of it can be printed nor downloaded.

Reference to Appendix E containing information on Academic Misconduct and Plagiarism. Assessment and Feedback Policy available at:

**[https://eng.asu.edu.eg/uploads/uploadcenter/asu\\_594\\_file.pdf](https://eng.asu.edu.eg/uploads/uploadcenter/asu_594_file.pdf)**

Assessment and feedback are fundamental parts of your learning experience. The UEL Assessment and Feedback Policy seeks to:

- actively promote student success and academic achievement;
- provide clear, accurate, accessible information and guidelines to all staff and students on assessment and feedback;
- maximise the potential for consistency and fairness in assessment;
- locate assessment and feedback as an integral part of learning and teaching processes.

Every component of assessment that contributes to an award, at all levels, is subject to internal and External Examiner moderation. This ensures the maintenance of standards both internally and in comparison, with similar programmes delivered at other higher education institutions. The UEL Assessment and Feedback Policy outlines the process for the various stages of the marking process and is available at **<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Assessment-and-Feedback-Policy>**

As a student you will be taught how to write correctly referenced essays using UEL's standard Harvard referencing system from Cite Them Right. Cite them Right is the standard Harvard referencing style at UEL for all Schools apart from the School of

Psychology which uses the APA system. This book will teach you all you need to know about Harvard referencing, plagiarism and collusion. The electronic version of "Cite Them Right: *the essential referencing guide*" 9th edition, can be accessed whilst on or off campus, via UEL Direct. The book can only be read online and no part of it can be printed nor downloaded.

*Further information is available at Appendix E and the weblinks below:*

*Harvard referencing*

***<https://uelac.sharepoint.com/LibraryandLearningServices/Pages/Harvard-Referencing-.aspx>***

*Academic Integrity*

***<https://uelac.sharepoint.com/LibraryandLearningServices/Pages/Academic-integrity.aspx>***

## **5.4 Assessment Criteria**

A student's performance will be marked and graded according to pre-specified and clear assessment criteria. These will normally be presented in one document combining marking and grading criteria. Further details can be found in section 2.3 of the Assessment and Feedback Policy and can be found at:

***<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/assessment-and-feedback-policy>***

As your degree progresses, you will be assessed in a number of different ways. In addition to examinations, you will have a range of coursework assessments such as reports or presentations, for which you will be given clear guidance by the module leader including how you will be assessed for that piece of work.

The section below gives you a general guideline of what we are looking for at different levels of the programme.

### **5.4.1 Level 3**

- You demonstrate understanding of factual information.
- you can process and evaluate given information and draw some conclusions.
- You can follow guidelines in developing solutions to simple problems.

### **5.4.2 Level 4**

- You can present factual information.
- You can analyse and evaluate the information presented and draw some conclusions.
- You can follow guidelines in creating solutions to straightforward problems.

*Work of a better standard usually reflects an approach where*

- You have required little additional guidance in producing your work.
- You have shown initiative where appropriate.



- You meet your obligations to others
- You have fully appreciated the complexity of a task and managed your time and resources accordingly.
- Your work is presented with care and forethought.

#### **5.4.3 Level 5**

- Your work displays a detailed knowledge of the topic. You are aware of other contexts that can be applied to this knowledge.
- With some guidance you can analyse data and situations in a range of different contexts.
- You can take information gathered or the ideas of others and re-format it to your own purpose.
- You can select appropriate evaluation techniques. You can use these to evaluate your own findings.

*Work of a better standard usually reflects an approach where*

- You have required minimal additional assistance
- You have been particularly creative in devising and implementing your chosen solution
- You have identified the key elements of problems and chosen the appropriate strategies to resolve them.
- You have communicated your work in a clear and concise manner.

#### **5.4.4 Level 6**

- Your work displays a comprehensive and detailed knowledge of the topic with areas of specialisation showing depth of understanding.
- You are aware of current developments.
- You can analyse data and situations in a range of different contexts.
- You can develop creative and innovative solutions with little guidance.
- You can review evidence critically and use your findings to support conclusions and recommendations.

*Work of a better standard usually reflects an approach where*

- You have not required any additional assistance
- You have proved you can manage your own learning and make full use of a wide range of resources.
- You have been confident in your ability to solve problems.
- You have communicated your work in a thoroughly professional and coherent manner.

The student must attend at least 75% of the course.

### **Research Integrity**

The University of East London conducts high quality, innovative research and is guided by the principles and standards outlined in The Concordat to Support Research

Integrity, 2012; the University's Code of Practice for Research; Code of Practice for Research Ethics and Procedures for the Investigation of Misconduct in Research, for staff and students. The Concordat seeks to provide a national framework for good research governance and its conduct, and applies to all fields of research supporting a research environment that is underpinned by ethical values. The University adheres to its responsibility to support and promote the highest standards of rigour and integrity and embed a culture of honesty, transparency and care and respect for all participants and subjects of research. The University is committed to ensuring that research is conducted with integrity and good research practices are upheld.

## **Research Ethics**

Research involving human participants, human material, including human tissue, embryos, foetuses and bodily fluids, from living or deceased participants, human data, personal, sensitive or otherwise, or non-human animal should comply with all legal and ethical requirements and other applicable guidelines. The University has established various Research Ethics Committees at University, School and College level to ensure appropriate ethical review of research projects involving human participation, human material or personal data. A proposed research study may require ethical approval from the main University Research Ethics Committee (UREC), one of the School Research Ethics Committees (SRECs), one of the College Research Ethics Boards (CREBs) or where applicable, Collaborative Partner Research Ethics Committees (CRECs). UREC reviews ethics applications from staff, MPhil, PhD, Post and Professional Doctorates and Masters by Research students. Please note, that Professional Doctorates from the School of Psychology are reviewed by the School of Psychology SREC. SRECs, CREBs and CRECs consider applications for ethical approval from taught Masters and undergraduate students.

Research involving human participation, human material or personal or sensitive data, where necessary, will require formal approval from UREC, SREC, CREB or CREC before the research commences. Students should submit research projects involving human participants, human material, personal or sensitive data or non-human animal for ethical review, to one of the University's Research Ethics Committees listed above, and abide by the outcome of the review. The application for ethical approval should be submitted alongside copies of any supporting documentation which will be given to the participants, including a Participant Information Sheet, Consent Form, interview schedule, indicative topic guide, self-completion survey or questionnaire, debrief letter, and recruitment poster, where appropriate.

The Research Ethics Committees ensure that appropriate procedures for obtaining informed consent are observed, having particular regard to the needs and capacity of the subjects involved. The dignity, rights, safety and well-being of participants must be the primary consideration in any research study. Appropriate care must be taken when research projects involve vulnerable groups, such as elderly people, children, people with mental ill-health, and covert studies or other forms of research which do not involve full disclosure of the research to participants. The University's Research Ethics Committees also ensure that research projects of this nature have been submitted for approval to all applicable external bodies; ethical, regulatory or otherwise.

**<https://uelac.sharepoint.com/ResearchInnovationandEnterprise/Pages/Ethics.aspx>**

Students who wish to conduct research in the NHS or Health and Social Care must apply to the NHS for ethical approval through the Integrated Research Application System (IRAS). The online application for ethical approval will be reviewed by a NHS or Social Care Research Ethics Committee. Students who are conducting research with only NHS staff or only using NHS premises are required to apply to the relevant University Research Ethics Committee; UREC, SREC, CREB or CREC and the Health Research Authority (HRA) for ethical approval. Students should seek guidance from their supervisor to ensure that ethical approval is sought from the appropriate body.

Students conducting studies under the auspices of any of the UK Departments of Health and/or the HRA are required to submit copies of their NHS, Social Care or HRA ethics approval letter, Local Information pack, IRAS application form and a PDF of the IRAS document checklist to the UREC Committee. UREC will grant consent for the study and issue a combined approval and sponsorship letter, for the research, on behalf of the University. The University acts as a sponsor for NHS or Social Care approved research projects, and students should conduct their studies in accordance with the conditions specified in the NHS, Social Care or HRA ethics approval letter. **<http://www.hra.nhs.uk/research-community/hra-approval-the-new-process-for-the-nhs-in-england>**

Students should understand their responsibilities to conduct research to high ethical standards and be aware of policies and procedures on good research practice. The University has established guidelines to preserve the confidentiality and security of personal data, relating to human participants and human material, involved in research projects. Students must comply with the regulations of appropriate regulatory or statutory bodies and any legal obligations when conducting or collaborating in research in other countries. The legal and ethical requirements existing in the UK and in the countries where the research will take place should also be observed. Students should ensure that they have fully prepared for their planned research, allowing enough time to submit an application for ethical approval and obtain appropriate consent. It is advisable that students seek guidance from supervisors on proposed research projects.

The University complies with all applicable Data Protection laws and students should consult the University's Data Management policies:

**<https://uelac.sharepoint.com/ITServices/Pages/Our-Policies.aspx>**

While preparatory activity is permitted, no formal contact with potential participants, recruitment of human participants or data collection for the research study may commence until ethical approval from UREC, SREC, CREB, CREC, or a NHS or Social Care Research Ethics Committee is confirmed. Students must conduct the research project in accordance with the University's policies and the conditions of ethical approval specified in the ethics approval letter, throughout the length of the study. Amendments to an approved research study must be submitted to the relevant Research Ethics Committee for review, and ethical approval obtained before any changes to the project may be implemented. Please be aware, ethical approval for

research projects cannot be granted retrospectively. Research conducted with human participants or human material, without ethical approval from the appropriate Research Ethics Committee, is considered misconduct in research and as such, students may be subject to formal investigation, which can result in the termination of the research project and misconduct proceedings.

**<https://uelac.sharepoint.com/ResearchInnovationandEnterprise/Pages/Ethics.aspx>**

## **Risk Assessment**

The University has a duty of care to its researchers and a responsibility to safeguard the welfare of research participants. Risk management should be considered at the same time as planning a research project. A comprehensive risk assessment helps to identify and evaluate potential hazards associated with the research project. Students in consultation with their supervisors should put control measures in place to minimise the likelihood of an event occurring that will cause harm. A risk assessment must be completed for research taking place within and outside of the University, fieldwork and research conducted overseas, before the project commences. The risk assessment should be completed by the student in collaboration with the supervisor and authorised by the Dean of the School or Associate/Acting Dean.

If students consider that human participants in their, or others,' research are subject to unreasonable risk or harm, they must report the concerns to their supervisor and, where necessary, to the appropriate regulatory authority. Similarly, concerns relating to the improper and/or unlicensed use or storage of human material or non-human animal or the improper use or storage of personal data, should also be reported.

Further guidance on risk assessments can be found in the University's Health & Safety Handbook:

**<https://uelac.sharepoint.com/HealthandSafetyUnit/Pages/H%26S-Handbook.aspx>**

## 6 MODULE SPECIFICATIONS

<b>Module Title:</b> Applied Mathematics for Communications Engineering	<b>UEL Module Code:</b> EG3XXX  <b>ASU Module Code:</b> EG10311  <b>Level:</b> 3  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b>  Dr. Makram Roshdy & Dr. Mohamed Abouelatta
<b>Pre-requisite:</b> None	<b>Pre-cursor:</b> None	
<b>Co-requisite:</b> None	<b>Excluded Combination:</b> None	
<b>Locations of delivery:</b> ASU		
<div><b>Main Aim(s) of the Module:</b><ul style="list-style-type: none"><li>• To provide students with an understanding of foundational mathematics material required for Communication and Electronics Systems Engineering.</li><li>• To prepare students for the more advanced mathematics they will encounter on their degree.</li></ul></div>		
<div><b>Main Topics of Study:</b><ul style="list-style-type: none"><li>• Vectors and Geometry of Space.</li><li>• Functions of several variables and Partial derivatives.</li><li>• Double integrals over rectangular regions and general regions</li><li>• Triple integrals in cylindrical and spherical coordinates.</li><li>• Line and surface integrals, Surface Integrals</li><li>• Green's theorems and Stokes' theorem.</li><li>• The Nature of probability and statistics</li><li>• Probability and counting rule.</li><li>• Frequency distributions and graphs</li><li>• Continuous probability distributions</li><li>• The divergence theorem.</li><li>• Basic Concepts and Rules of Probability.</li><li>• Discrete and continuous random variables and its distributions</li><li>• Organization data, Frequency Distributions, Cumulative frequency distribution and graphs.</li><li>• Measures of central tendency and measures of dispersion.</li><li>• Coefficient of variation and coefficient of skewness.</li><li>• Introduction to calcification of differential equations, first order equations</li><li>• Higher order differential equations: homogenous</li><li>• Cauchy-Euler equations, Laplace transform, Fourier series and Partial differential equations.</li><li>• Solving improper integrals in terms of the Gamma and Beta functions, Fourier series.</li><li>• Solve special kinds and forms of differential equations in terms of Bessel functions and Legendre polynomials.</li><li>• Solid foundation in functions of complex variables.</li></ul></div>		
<b>Learning Outcomes for the Module</b>  At the end of this module, students will be able to:  Knowledge 1. Recognize the basic principles in probability and statistics.		

<p>2. Identify different complex functions.</p> <p>Thinking skills</p> <p>3. Analyse signals using Fourier analysis.</p> <p>4. Solve mathematical problems numerically.</p> <p>Subject-based practical skills</p> <p>5. Use the knowledge of multiple integrals and statistics to solve practical engineering problems;</p> <p>6. Use mathematical models to solve differential equations and partial differential equations.</p> <p>Skills for life and work (general skills)</p> <p>7. Operate effectively in a team;</p> <p>8. Develop the skills which are related to creative thinking, problem solver in different fields.</p>		
<p><b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b></p> <p>"Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work."</p>		
<p><b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b></p> <p><b>Portfolio</b> Continuous assessment tasks that include:</p> <p>Portfolio 1 (52 hours)</p> <p>Portfolio 2 (52 hours)</p> <p>Portfolio 3 (52 hours)</p> <p>Portfolio 4 (52 hours)</p> <p>Each portfolio in the module contains:</p> <ul style="list-style-type: none"> <li>• Participation</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>	<p><b>Weighting:</b></p> <p>25%</p> <p>25%</p> <p>25%</p> <p>25%</p>	<p><b>Learning Outcomes demonstrated</b></p> <p>1,5, 7, 8</p> <p>6-8</p> <p>4, 7, 8</p> <p>2-3, 7, 8</p>
<p><b>Reading and resources for the module:</b></p> <p><b>Core</b></p> <ul style="list-style-type: none"> <li>- Stewart, J. (2015) <i>Calculus</i>. 8<sup>th</sup> edn. Cengage Learning.</li> <li>- Stroud, K. A., &amp; Booth, D. J. (2013) <i>Engineering mathematics</i>. 7<sup>th</sup> edn. Macmillan International Higher Education.</li> <li>- Wackerly, D., &amp; Scheaffer, W. (2008) <i>Mathematical Statistics with Applications</i>. 7<sup>th</sup> edn. Thomson Brooks/Cole.</li> </ul> <p><b>Recommended</b></p> <ul style="list-style-type: none"> <li>- Kreyszig, E. (2011) <i>Advanced engineering mathematics</i>. 10<sup>th</sup> edn. John Wiley &amp; Sons.</li> </ul>		
<p><b>Indicative Teaching and Learning Time (10 hrs per credit):</b></p>	<p><b>Activity</b></p>	

1.Student/Tutor interaction, some of which may be online:  200 hours	Lectures/ Tutorials/ Practicals
2.Student Learning Time:  100 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.
Total hours (1 and 2):	300 hours

<b>Module Title:</b> Foundations of Physics	<b>UEL Module Code:</b> EG3XXX  <b>ASU Module Code:</b> EG10312  <b>Level:</b> 3  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b>  Prof. Wael Fikry & Dr. Mohamed Abouelatta
<b>Pre-requisite:</b> None	<b>Pre-cursor:</b> None	
<b>Co-requisite:</b> None	<b>Excluded Combination:</b> None	
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To provide the fundamental skills necessary to distinguish between different classes of materials.</li><li>• To outline the origin of mechanical, electrical, thermal, optical and magnetic properties and explain how they are related to their structure.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Explain the concepts of oscillations and waves.</li><li>• Differentiate between mechanical waves and electromagnetic waves</li><li>• Recognize different phenomena related to waves such as interference and diffraction.</li><li>• Identify different states of polarization.</li><li>• Interpret the basic thermodynamics terminology.</li><li>• Implement the laws of thermodynamics to provide explanations for practical applications.</li><li>• Perform laboratory experiments to realize optical waves phenomena and measure the corresponding parameters.</li><li>• Modern physics: Plank's theory of quantization of energy of radiation,</li><li>• Photo-electric effect, x-rays and Compton's effect, Wave properties of matter and wave function,</li><li>• Principles of quantum mechanics and Schrödinger equation,</li><li>• Atomic structure and study of the tunnelling phenomenon.</li><li>• Solution of Schrödinger equations in Quantum well, quantum dot and periodic structures.</li><li>• Bloch function, Kronig-Penny model. Quantum theory of free electrons in metals, Statistical distribution laws. Fermi-Dirac Distribution.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Interpret the concept of oscillations, wave interference and diffraction;</li><li>2. Explain important phenomena in modern physics and the nature of the quantum mechanics.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>3. Implement wave interference and diffraction principles in simple system designs;</li><li>4. Apply quantum mechanics techniques to solve quantum mechanical systems.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>5. Design oscillatory electrical systems and heat engine;</li><li>6. Shows the ability to differentiate between quantum and classical systems.</li></ol>		



Skills for life and work (general skills) 7. Write a technical report and present using digital libraries; 8. Undertake independent research.		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>  Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.		
<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include:  Portfolio 1 (105 hours)  Portfolio 2 (105 hours)  Each portfolio in the module contains: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Practical demonstrations</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>	<b>Weighting:</b>          50%          50%	<b>Learning Outcomes demonstrated</b>          1,3,5,7,8          2,4,6,7,8
Reading and resources for the module:  <b>Core</b> <ul style="list-style-type: none"> <li>- Serway, R. A., &amp; Jewett, J. W. (2018) <i>Physics for scientists and engineers with modern physics</i>. 10<sup>th</sup> edn. Cengage learning.</li> <li>- Neamen, D. A. (2012) <i>Semiconductor physics and devices: basic principles</i>. 4<sup>th</sup> edn. New York, NY: McGraw-Hill.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Young, H. D., &amp; Freedman, R. A. (2015) <i>University physics with modern physics</i>. 14<sup>th</sup> edn. Pearson Higher Ed.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:  200 hours	Lectures/ Tutorials/Practicals	
2.Student Learning Time:  100 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	300 hours	

<b>Module Title:</b> Engineering and Electronic Materials		<b>UEL Module Code:</b> EG3XXX  <b>ASU Module Code:</b> EG10313  <b>Level:</b> 3  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b> Dr. Mohamed Abouelatta
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None	
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None	
<b>Locations of delivery:</b> ASU			
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To provide the fundamental skills necessary to distinguish between different classes of materials.</li><li>• To provide the basic principles of solid-state electronic materials.</li><li>• To outline the origin of mechanical, electrical, thermal, optical and magnetic properties and explain how they are related to their structure.</li></ul>			
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Classification of materials.</li><li>• Atomic structure and bonding in solids.</li><li>• Metallic' Structure.</li><li>• Ceramics' structure.</li><li>• Structure of Polymers.</li><li>• Imperfection in Metals and Ceramics.</li><li>• Mechanical properties.</li><li>• Electron in a periodic potential, energy bands and energy gaps in solids.</li><li>• Semiconductors, the Fermi level, electrons and holes.</li><li>• Intrinsic and extrinsic semiconductors.</li><li>• n-type and p-type, Diffusion and Drift Current.</li><li>• Excess carriers in semiconductors, Optical generation and recombination, the continuity equation, nonhomogeneous doping.</li><li>• PN-junction: I-V characteristics, Reverse saturation current depletion layer capacitance, Diffusion capacitance, Zener diodes.</li></ul>			
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ul style="list-style-type: none"><li>1. Distinguish different classes of materials and different types of structure;</li><li>2. Identify the different properties of semiconductor materials and the concept of energy bands.</li></ul> <p>Thinking skills</p> <ul style="list-style-type: none"><li>3. Outline the origin of mechanical, electrical, thermal, optical and magnetic properties and explain how they are related to their structure;</li><li>4. Evaluate the characteristics of semiconductors under equilibrium and nonequilibrium conditions and the characteristics of a PN junction.</li></ul> <p>Subject-based practical skills</p> <ul style="list-style-type: none"><li>5. Making practical building blocks for various types of crystal structures;</li></ul>			

6. Write MATLAB programs for Kronig-Penney model, Fermi distribution function and related mathematical functions.  Skills for life and work (general skills) 7. Write a technical report and presentation using digital libraries; 8. Undertake independent research.		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>  Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.		
<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include:  Portfolio 1 (105 hours)  Portfolio 2 (105 hours)  Each portfolio in the module contains: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Practical demonstrations</li> <li>• TCAD Simulation Report</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>	<b>Weighting:</b>  50%  50%	<b>Learning Outcomes demonstrated</b>  1,3,7,8  2,4-8
Reading and resources for the module:  <b>Core</b> <ul style="list-style-type: none"> <li>- Callister Jr, W. D., &amp; Rethwisch, D. G. (2015) <i>Fundamentals of Materials Science and Engineering</i>. 5<sup>th</sup> edn. John Wiley and Sons Inc.</li> <li>- Neamen, D. A. (2012) <i>Semiconductor physics and devices: basic principles</i>. 4<sup>th</sup> edn. New York, NY: McGraw-Hill.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Shackelford, J. F. (2014) <i>Introduction to Materials Science for Engineers</i>. 8<sup>th</sup> edn. Prentice-Hall.</li> <li>- Kasap, S. O. (2006) <i>Principles of electronic materials and devices</i> (Vol. 2) .3<sup>rd</sup> edn. New York: McGraw-Hill.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:  200 hours	Lectures/ Tutorials/Practicals	
2.Student Learning Time:  100 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	300 hours	

<b>Module Title:</b> Circuits and Fields theory	<b>UEL Module Code:</b> EG3XXX  <b>ASU Module Code:</b> EG10314  <b>Level:</b> 3  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b>  Dr. Angie Reda Eldamak & Dr. Mohamed Abouelatta
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To develop the ability to analyse and solve dc and ac circuits.</li><li>• To solve circuit problems using computer facilities.</li><li>• To explain the relevant mathematical tools required for further study in the field of electromagnetic waves.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Circuit Theorems.</li><li>• Basic Circuit Laws.</li><li>• Electrical Circuits Theorems, Nodal Analysis.</li><li>• Mesh Analysis and Superposition Theorem.</li><li>• Thevenin's and Norton's Theorem.</li><li>• Inductors and Capacitors.</li><li>• Analysis of Sinusoidal Steady State Circuits.</li><li>• Resonance in Electric Circuits.</li><li>• Response of First and Second Order RL-C Circuits.</li><li>• Two-Port and Filter Networks.</li><li>• Magnetically Coupled Circuits.</li><li>• Coulomb's Law and Electric Field Intensity.</li><li>• Field of Point Charge, Line Charge, Surface Charge, and Continuous Volume Charge.</li><li>• Electric Flux, Gauss's Law, Divergence, Electric Energy and Potential.</li><li>• Dielectrics and Boundary Conditions.</li><li>• Steady Magnetic Fields and Ampere's Law.</li><li>• Magnetic Materials, Magnetic flux and Inductance.</li><li>• Magnetic Boundary Conditions.</li><li>• Time-Varying Fields, Faraday's Law and Maxwell's Equations.</li></ul>		
<b>Learning Outcomes for the Module</b> At the end of this module, students will be able to:  Knowledge <ul style="list-style-type: none"><li>1. Define the different theories that can be applied to electric circuits;</li><li>2. Recognize the main principles of electromagnetism as a foundation for electrical engineering.</li></ul> Thinking skills <ul style="list-style-type: none"><li>3. Apply the different theories to solve electrical circuits;</li><li>4. Select the relevant scientific arguments when explaining and applying electromagnetic concepts.</li></ul>		

<p>Subject-based practical skills</p> <ol style="list-style-type: none"> <li>5. Select the appropriate analysis technique to solve electrical circuits with direct and time varying sources in the steady state;</li> <li>6. Estimate key quantities occurring in practical electromagnetic problems.</li> </ol> <p>Skills for life and work (general skills)</p> <ol style="list-style-type: none"> <li>7. Work and communicate effectively in team;</li> <li>8. Develop skills related to creative thinking and problem solving.</li> </ol>		
<p><b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b></p> <p>Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.</p>		
<p><b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b></p> <p><b>Portfolio</b> Continuous assessment tasks that include:</p> <p>Portfolio 1 (105 hours)</p> <p>Portfolio 2 (105 hours)</p> <p>Each portfolio in the module contains:</p> <ul style="list-style-type: none"> <li>• Participation</li> <li>• Reports &amp; Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>	<p><b>Weighting:</b></p> <p>50%</p> <p>50%</p>	<p><b>Learning Outcomes demonstrated</b></p> <p>1,3,5,7,8</p> <p>2,4,6-8</p>
<p><b>Reading and resources for the module:</b></p> <p><b>Core</b></p> <ul style="list-style-type: none"> <li>- Irwin, J. D., &amp; Nelms, R. M. (2011) <i>Basic engineering circuit analysis</i> (Vol. 900) .10<sup>th</sup> edn. John Wiley &amp; Sons.</li> <li>- Ulaby, F. T., Michielssen, E., &amp; Ravaioli, U. (2014) <i>Fundamentals of applied electromagnetics</i>.7<sup>th</sup> edn. Boston, Massachusetts: Prentice Hall.</li> </ul> <p><b>Recommended</b></p> <ul style="list-style-type: none"> <li>- Nilsson, J. W. (2008) <i>Electric circuits</i>. 8<sup>th</sup> edn. Pearson Education India.</li> <li>- Hayt, W. H., Kemmerly, J. E., &amp; Durbin, S. M. (2007) <i>Engineering Circuit Analysis</i>. 7<sup>th</sup> edn. Mc Graw Hill.</li> <li>- Alexander, C. K., &amp; no Sadiku, M. (2009) <i>Fundamental of electrical circuits</i>. 4<sup>th</sup> edn. Mc Graw Hill.</li> <li>- Hayt, W. H., &amp; Buck, J. A. (2011) <i>Engineering electromagnetics</i>. 8<sup>th</sup> edn. New York: McGraw-Hill.</li> </ul>		
<p><b>Indicative Teaching and Learning Time (10 hrs per credit):</b></p> <p>1.Student/Tutor interaction, some of which may be online:</p> <p>200 hours</p>	<p><b>Activity</b></p> <p>Lectures/ Tutorials/ Practicals</p>	

2.Student Learning Time:  100 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.
Total hours (1 and 2):	300 hours

<b>Module Title:</b> Digital Design and Computer architecture	<b>UEL Module Code:</b> EG4XXX  <b>ASU Module Code:</b> EG10421  <b>Level:</b> 4  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b> Dr. Mohamed Taher & Dr. Mohamed Abouelatta
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To design algorithms and combinational and sequential logic circuits.</li><li>• To learn about memory-based systems.</li><li>• To provide the fundamental skills necessary to describe the structure and behaviour of digital computers at several levels of abstraction.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Introduction to computer algorithms and flow charts.</li><li>• Basic Structure of C++ program.</li><li>• The conditional statement, the for loop, the while loop, the do while loop.</li><li>• Arrays definition, programming with Arrays.</li><li>• Pointer Overview.</li><li>• Structures.</li><li>• Introduction to digital and number systems.</li><li>• Design of combinational circuits using AND/OR/Inverters and NAND/NOR gates.</li><li>• Arithmetic circuits: adders and subtractors.</li><li>• Multiplexers, encoders, and decoders.</li><li>• Storage devices: FFs, edge triggered FFs, master-slave FFs, and excitation tables.</li><li>• State tables and transition diagrams.</li><li>• Design of sequential circuits, counters, and shift registers.</li><li>• The MIPS instruction set and assembly programming.</li><li>• MIPS single-cycle and pipelined implementation.</li><li>• Exceptions and interrupts.</li><li>• Superscalar architectures.</li><li>• Caching and Virtual Memory.</li></ul>		
<b>Learning Outcomes for the Module</b> At the end of this module, students will be able to:  Knowledge <ul style="list-style-type: none"><li>1. Explain the programming concepts and different data structures and logic gates;</li><li>2. Explain how computers execute programs and how a data-path with or without pipelining is designed.</li></ul> Thinking skills <ul style="list-style-type: none"><li>3. Develop map problems into circuits;</li><li>4. Translate C programs to assembly and assembly programs to machine code.</li></ul> Subject-based practical skills <ul style="list-style-type: none"><li>5. Manage computer systems resources;</li></ul>		

6. Utilize Logisim to design and simulate digital circuits. Skills for life and work (general skills) 7. Undertake independent research. 8. Work and communicate effectively in team by effective collaboration and task management.		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>  Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.		
<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include:  Portfolio 1 (63 hours)  Portfolio 2 (73 hours)  Portfolio 3 (73 hours)  Each portfolio in the module contains: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Practical demonstrations</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>	<b>Weighting:</b>   30%  35%  35%	<b>Learning Outcomes demonstrated</b>   1, 4,7,8  3, 6-8  2, 5,7,8
<b>Reading and resources for the module:</b>  <b>Core</b> <ul style="list-style-type: none"> <li>- Malik, D. S. (2012) <i>C++ Programming: From Problem Analysis to Program Design</i>. 6<sup>th</sup> edn. Course Technology.</li> <li>- Mano, M.M. &amp; Ciletti, M. D. (2013) <i>Digital Design</i>. 5<sup>th</sup> edn. Prentice Hall.</li> <li>- Patterson, D. A. &amp; Hennessy, J. L. (2016) <i>Computer Organization and Design: The Hardware/Software Interface</i>. 5<sup>th</sup> edn. Elsevier.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Brown, S. &amp; Vranesic, Z. (2005) <i>Fundamentals of Digital Logic with VHDL Design</i>. 2<sup>nd</sup> edn. McGraw Hill.</li> <li>- Katz, R. H. &amp; Borriello, G. (2005) <i>Contemporary Logic Design</i>. 2<sup>nd</sup> edn. Prentice Hall.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:  200 hours	Lectures/ Tutorials/Practicals	
2.Student Learning Time:  100 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	300 hours	



<b>Module Title:</b> Electronic Devices & Circuits	<b>UEL Module Code:</b> EG4XXX  <b>ASU Module Code:</b> EG10422  <b>Level:</b> 4  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b> Dr. Mohamed Abouelatta
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To provide foundational background, theories and mechanism of operation of principle semiconductor devices with emphasis on physical concepts.</li><li>• To provide good understanding of the link between device physics and circuit design by introducing to them design parameters, performance parameters, CAD models, and device scaling theory, etc.</li><li>• To build background in issues related to microelectronic circuits.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Introduction to Semiconductor Devices.</li><li>• BJTs.</li><li>• JFETs and MESFETs.</li><li>• MOSFETs.</li><li>• MOSFET Applications.</li><li>• Device simulators.</li><li>• Diode Models and Circuits.</li><li>• Bipolar Transistors Physics and Amplifiers.</li><li>• MOS Transistors Physics and Amplifiers.</li><li>• OPAMP Circuits.</li><li>• Introduction to Filters, ADCS, DACs, and PLLs.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Identify the mechanisms of operation of principle semiconductor devices;</li><li>2. Recognize the different BJT and MOS amplifier topologies.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>3. Develop analytical models for semiconductor devices;</li><li>4. Analyse and design diode circuits, BJT and MOS amplifiers.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>5. Design different amplifier circuits meeting certain specifications;</li><li>6. Build and measure practical experiments for diode, BJT, MOS, and Opamp circuits.</li></ol> <p>Skills for life and work (general skills)</p> <ol style="list-style-type: none"><li>7. Write a technical report and presentation using digital libraries;</li></ol>		

8. Undertake independent research.		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>  Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.		
<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include:  Portfolio 1 (105 hours)  Portfolio 2 (105 hours)  Each portfolio in the module contains: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Practical demonstrations</li> <li>• Assignments</li> <li>• TCAD Simulation Report</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>	<b>Weighting:</b>   50%  50%	<b>Learning Outcomes demonstrated</b>   1, 3,7,8  2, 4-8
Reading and resources for the module:  <b>Core</b> <ul style="list-style-type: none"> <li>- Streetman, B. G. &amp; Banerjee, S. K. (2006) <i>Solid State Electronic Devices</i>. 6<sup>th</sup> edn. Prentice-Hall, New Jersey.</li> <li>- Sedra, A. S., &amp; Smith, K. C. (2016) <i>Microelectronic circuits</i>. 7<sup>th</sup> edn. Oxford University Press.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Yang, E. S. (1988) <i>Microelectronic devices</i>. New York: McGraw-Hill.</li> <li>- Razavi, B. (2013) <i>Fundamentals of microelectronics</i>. 2<sup>nd</sup> edn. John Wiley &amp; Sons.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:  200 hours	Lectures/ Tutorials/Practicals	
2.Student Learning Time:  100 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	300 hours	

<b>Module Title:</b> Fundamentals of Microwave Engineering	<b>UEL Module Code:</b> EG4XXX  <b>ASU Module Code:</b> EG10423  <b>Level:</b> 4  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b> Prof. Amr Safwat
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To define and recognize basic concepts in electromagnetic waves.</li><li>• To explain the behaviour of electromagnetic waves propagation in the presence of material media and their interfaces.</li><li>• To recognize the fundamental properties of high frequency transmission lines.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Plane electromagnetic waves.</li><li>• Propagation of electromagnetic waves in matter.</li><li>• Power and energy relations.</li><li>• Reflection and refraction.</li><li>• Transmission line Theory.</li><li>• Transverse Electro-Magnetic Waves and Power flow on TL.</li><li>• Circuit analysis of transmission lines.</li><li>• Graphical solution of transmission lines (Smith chart).</li><li>• Impedance matching using Smith chart.</li><li>• Generalized Maxwell's equations and parallel plate waveguide.</li><li>• Closed waveguides (rectangular and circular).</li><li>• Microstrip transmission line.</li><li>• Symmetric and asymmetric dielectric slab planar waveguide and step-index optical fibre.</li><li>• Equivalent circuit of waveguide. N- port circuit description.</li><li>• Scattering parameters of waveguides and waveguide coupling by aperture.</li><li>• Resonators.</li><li>• Passive devices: Terminations, attenuators, directional couplers, hybrid junctions, and nonreciprocal devices.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Identify fundamental properties of high frequency transmission lines for communication systems;</li><li>2. Analyse wave propagation in different waveguides and optical fibers.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>3. Use appropriate mathematical tools and techniques for analyzing range of electromagnetic waves related problems;</li><li>4. Evaluate the characteristics of transmission lines.</li></ol>		

<p>Subject-based practical skills</p> <ol style="list-style-type: none"> <li>Implement mathematical techniques to solve electromagnetic wave propagation and transmission line related problems;</li> <li>Develop microwave circuits measurement skills.</li> </ol> <p>Skills for life and work (general skills)</p> <ol style="list-style-type: none"> <li>Work in a team and share ideas to achieve a task in a timely manner;</li> <li>Test microwave circuits.</li> </ol>		
<p><b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b></p> <p>Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.</p>		
<p><b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b></p> <p><b>Portfolio</b> Continuous assessment tasks that include:</p> <p>Portfolio 1 (105 hours)</p> <p>Portfolio 2 (105 hours)</p> <p>Each portfolio in the module contains:</p> <ul style="list-style-type: none"> <li>Participation</li> <li>Practical demonstrations</li> <li>Assignments</li> <li>Quizzes</li> <li>A midterm exam</li> <li>Final exam</li> </ul>	<p><b>Weighting:</b></p> <p>50 %</p> <p>50%</p>	<p><b>Learning Outcomes demonstrated</b></p> <p>3, 5,7</p> <p>1, 2, 4, 6-8</p>
<p>Reading and resources for the module:</p> <p><b>Core</b></p> <ul style="list-style-type: none"> <li>Hayt, W. H., &amp; Buck, J. A. (2001) <i>Engineering electromagnetics</i>. 6<sup>th</sup> edn. New York: McGraw-Hill.</li> <li>Pozar, D. M. (2011) <i>Microwave engineering</i>. 4<sup>th</sup> edn. John Wiley &amp; Sons.</li> </ul> <p><b>Recommended</b></p> <ul style="list-style-type: none"> <li>Ulaby, F. T., Michielssen, E., &amp; Ravaioli, U. (2014) <i>Fundamentals of applied electromagnetics</i>. 7<sup>th</sup> edn. Boston, Massachusetts: Prentice Hall.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:		
200 hours	Lectures/ Tutorials/Practicals	
2.Student Learning Time:		
100 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	300 hours	

<b>Module Title:</b> Fundamentals of signals & Communications	<b>UEL Module Code:</b> EG4XXX  <b>ASU Module Code:</b> EG10424  <b>Level:</b> 4  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b> Dr. Hussein Abdel Atty Elsayed
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To provide good understanding of the basic concepts of signals and systems.</li><li>• To develop the student's ability to analyse digital signals and systems both in time and frequency domains.</li><li>• To build background in issues related to communication systems.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Signals types.</li><li>• Basic system properties.</li><li>• Linear time-invariant systems.</li><li>• Convolution.</li><li>• Fourier representation signals.</li><li>• Z-transform.</li><li>• Sampling theorem.</li><li>• Digital filters.</li><li>• FIR filter.</li><li>• IIR filter.</li><li>• Amplitude modulation.</li><li>• Frequency modulation.</li><li>• Phase modulation.</li><li>• Pulse modulation systems.</li><li>• Heterodyne receiver.</li><li>• TV broadcasting system.</li><li>• Random Processes and noise.</li></ul>		
<b>Learning Outcomes for the Module</b> At the end of this module, students will be able to:  Knowledge <ul style="list-style-type: none"><li>1. Identify the different types of signals and system types;</li><li>2. Explain the operation of modulation and demodulation techniques (AM, FM, and PM);</li><li>3. Describe the effect of the noise on the communication systems.</li></ul> Thinking skills <ul style="list-style-type: none"><li>4. Analyse discrete-time systems both in time and frequency domains;</li><li>5. Analyse the individual building blocks of analog communication systems.</li></ul> Subject-based practical skills <ul style="list-style-type: none"><li>6. Simulate using MATLAB to get the effect of the systems on different types of signals;</li></ul>		

<p>7. Conduct laboratory experiments to investigate the modulators and demodulator operation.</p> <p>Skills for life and work (general skills)</p> <p>8. Write reports about experiments' simulation results;</p>		
<p><b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b></p> <p>Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.</p>		
<p><b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b></p> <p><b>Portfolio</b> Continuous assessment tasks that include:</p> <p>Portfolio 1 (73 hours)</p> <p>Portfolio 2 (73 hours)</p> <p>Portfolio 3 (63 hours)</p> <p>Each portfolio in the module contains:</p> <ul style="list-style-type: none"> <li>• Participation</li> <li>• Practical demonstrations</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>	<p><b>Weighting:</b></p> <p>35%</p> <p>35%</p> <p>30%</p>	<p><b>Learning Outcomes demonstrated</b></p> <p>1, 6</p> <p>2-3, 5,7,8</p> <p>4</p>
<p>Reading and resources for the module:</p> <p><b>Core</b></p> <ul style="list-style-type: none"> <li>- Haykin, S., &amp; Van Veen, B. (2007) <i>Signals and systems</i>. 2<sup>nd</sup> edn. John Wiley &amp; Sons.</li> <li>- Manolakis, D. G., &amp; Ingle, V. K. (2011) <i>Applied digital signal processing: theory and practice</i>. 1<sup>st</sup> edn. Cambridge University Press.</li> <li>- Haykin, S. (2009) <i>Communication Systems</i>. 5<sup>th</sup> edn. John Wiley &amp; Sons.</li> </ul> <p><b>Recommended</b></p> <ul style="list-style-type: none"> <li>- Oppenheim, A.V., Willsky, A.S. &amp; Nawab, S.H. (1997) <i>Signals and Systems</i>. 2<sup>nd</sup> edn. Prentice-Hall.</li> <li>- Couch, L. W., Kulkarni, M., &amp; Acharya, U. S. (2013) <i>Digital and analog communication systems</i>. 8<sup>th</sup> edn. Upper Saddle River, NJ: Prentice Hall.</li> </ul>		
<p><b>Indicative Teaching and Learning Time (10 hrs per credit):</b></p>	<p><b>Activity</b></p>	
<p>1.Student/Tutor interaction, some of which may be online:</p> <p>200 hours</p>	<p>Lectures/ Tutorials/Practicals</p>	
<p>2.Student Learning Time:</p> <p>100 hours</p>	<p>Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.</p>	
<p>Total hours (1 and 2):</p>	<p>300 hours</p>	

<b>Module Title:</b> Analog and Digital Circuits	<b>UEL Module Code:</b> EG5XXX  <b>ASU Module Code:</b> EG10531  <b>Level:</b> 5  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b> Dr. Sameh Ibrahim
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To understand analogue circuits.</li><li>• To understand, analyse and design different digital gates.</li><li>• To design and optimize any analogue circuit.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Analysis and design of differential amplifiers in bipolar and CMOS technologies.</li><li>• Current mirrors and active loads. Frequency response of amplifiers.</li><li>• Feedback and its properties.</li><li>• Stability issues and frequency compensation.</li><li>• Oscillators and Voltage Controlled Oscillators.</li><li>• CMOS Inverter: Noise margin, Propagation delay, Power dissipation</li><li>• CMOS combinational circuits: Static design, Pass transistors and transmission gates, Dynamic design.</li><li>• CMOS sequential circuits: Latches, Flip-flops, Counters, Monostable Ring oscillator,</li><li>• Random Access Memory RAM and Read Only Memory ROM</li><li>• Datapath building blocks: Shifters, adders, and multipliers.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Distinguish main building blocks and the various pros and cons of different analog circuits;</li><li>2. Compare different digital circuit families;</li><li>3. Determine the operation of semiconductor memories and Datapath blocks.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>4. Design a variety of multifunction analog circuits;</li><li>5. Evaluate the performance of static and dynamic logic circuits.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>6. Design the device parameters to meet the required functionality from analog circuit;</li><li>7. Create simulation testbenches for digital circuits.</li></ol> <p>Skills for life and work (general skills)</p> <ol style="list-style-type: none"><li>8. Propose different useful simulation tools.</li></ol>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>		

Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.

**Assessment methods which enable students to demonstrate the learning outcomes for the module:**

**Weighting:**

**Learning Outcomes demonstrated**

**Portfolio**

Continuous assessment tasks that include:

Portfolio 1 (105 hours)

50%

1,4,6,8

Portfolio 2 (105 hours)

50%

2,3,5, 7,8

Each portfolio in the module contains:

- Participation
- Assignments
- Project
- Quizzes
- A midterm exam
- Final Exam

Reading and resources for the module:

**Core**

- Sedra, A. S., & Smith, K. C. (2016) *Microelectronic circuits*. 7th edn. Oxford University Press.
- Rabaey, J. M., Chandrakasan, A. P., & Nikolic, B. (2003) *Digital integrated circuits*. 2<sup>nd</sup> edn. Englewood Cliffs: Prentice hall.

**Recommended**

- Johns, D. & Martin, K. (2011) *Analog Integrated Circuit Design*. 2nd edn. Jon Wiley & sons.
- Sansen, W. M. (2007) *Analog design essentials*. 1st edn. Springer Science & Business Media.
- N. H., & Harris, D. (2015) *CMOS VLSI design: a circuits and systems perspective*. 4th edn. Pearson Education India.

**Indicative Teaching and Learning Time (10 hrs per credit):**

**Activity**

1.Student/Tutor interaction, some of which may be online:

200 hours

Lectures/ Tutorials/Practicals

2.Student Learning Time:

100 hours

Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.

Total hours (1 and 2):

300 hours



<b>Module Title:</b> Antenna Engineering	<b>UEL Module Code:</b> EG5XXX  <b>ASU Module Code:</b> EG10532  <b>Level:</b> 5  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b>  Prof. Amr Safwat
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To enrich knowledge in the field of antennas.</li><li>• To develop students' ability to analyse basic antennas structures.</li><li>• To develop students' ability to design simple antennas.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Infinitesimal dipole.</li><li>• Antennas parameters.</li><li>• Wire antennas.</li><li>• Feeding networks and ground effects.</li><li>• Loop antennas.</li><li>• Uniform arrays.</li><li>• Non-uniform arrays.</li><li>• Aperture antennas.</li><li>• Rectangular and circular apertures.</li><li>• Reflector antennas.</li><li>• Broadband antennas.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Demonstrate the antenna fundamental parameters;</li><li>2. Examine the operation of antennas arrays;</li><li>3. Interpret the operation of broadband antennas.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>4. Differentiate various antennas structures;</li><li>5. Select appropriate specifications for simple antennas structures.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>6. Design simple antennas structures.</li></ol> <p>Skills for life and work (general skills)</p> <ol style="list-style-type: none"><li>7. Work in a team and share ideas to achieve a task in a timely manner;</li><li>8. Undertake independent research.</li></ol>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>		

Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.

**Assessment methods which enable students to demonstrate the learning outcomes for the module:**

**Weighting:**

**Learning Outcomes demonstrated**

**Portfolio**

Continuous assessment tasks that include:

Final Exam

Coursework divided as:

- Participation
- Lab Reports
- Assignments
- Quizzes
- A midterm exam
- Final Exam

(105 hours)

100%

1- 8

Reading and resources for the module:

**Core**

- Balanis, C. A. (2016) *Antenna theory: analysis and design*. 4<sup>th</sup> edn. John Wiley & sons.

**Recommended**

- Volakis, J. L. (2019) *Antenna engineering handbook*. 5<sup>th</sup> edn. McGraw-Hill Education.
- Russer, P. (2006) *Electromagnetics, microwave circuit and antenna design for communications engineering*. 2<sup>nd</sup> edn. Artech House.
- Chen, Y., & Wang, C. F. (2015) *Characteristic modes: Theory and applications in antenna engineering*. John Wiley & Sons.

**Indicative Teaching and Learning Time (10 hrs per credit):**

**Activity**

1.Student/Tutor interaction, some of which may be online:

100 hours

Lectures/ Tutorials/Practicals

2.Student Learning Time:

50 hours

Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.

Total hours (1 and 2):

150 hours

<b>Module Title:</b> Digital Transmission and Networks	<b>UEL Module Code:</b> EG5XXX  <b>ASU Module Code:</b> EG10533  <b>Level:</b> 5  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b> Dr. Hussein Abdel Atty Elsayed
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To provide good understanding of pulse code modulation.</li><li>• To explain spread spectrum techniques.</li><li>• To understand the basic terminologies and the operation of some communication networks.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Sampling Process.</li><li>• Pulse Amplitude Modulation.</li><li>• Pulse Code modulation.</li><li>• Interference.</li><li>• Nyquist criterion.</li><li>• Repeaters.</li><li>• Signal space analysis.</li><li>• ASK.</li><li>• FSK.</li><li>• PSK.</li><li>• Network topology.</li><li>• Switching.</li><li>• PSTN.</li><li>• Network multiplexing.</li><li>• DSL.</li><li>• Transmission media.</li><li>• Network applications.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Differentiate digital modulation techniques;</li><li>2. Compare the different types of network topologies.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>3. Design passband digital transmission systems;</li><li>4. Evaluate the network end to end delay.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>5. Experiment using MATLAB to get the effect of the systems on different types digital modulation (by conducting laboratory experiments);</li></ol>		

6. Design multiplexing/demultiplexing system to meet certain requirements.  Skills for life and work (general skills) 7. Work in a team and share ideas to achieve a task in a timely manner; 8. Write reports about experiments' simulation results.		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>  Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.		
<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include:  Portfolio 1 (105 hours)  Portfolio 2 (105 hours)  Each portfolio in the module contains: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab Reports (if laboratory exists)</li> <li>• Project</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>	<b>Weighting:</b>          50%          50%	<b>Learning Outcomes demonstrated</b>          1, 3, 5-8          2, 4, 7
Reading and resources for the module:  <b>Core</b> <ul style="list-style-type: none"> <li>- Haykin, S. (2009) <i>Communication systems</i>. 5<sup>th</sup> edn. John Wiley &amp; Sons.</li> <li>- Tanenbaum, A. S. (2003) <i>Computer networks</i>. 4<sup>th</sup> edn. ed: Prentice Hall.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Proakis, J. G., &amp; Salehi, M. (2008) <i>Digital communications</i>. 5<sup>th</sup> edn. New York: McGraw-hill.</li> <li>- Stallings, W. (2007) <i>Data and computer communications</i>. 8<sup>th</sup> edn. Pearson Education India.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:  200 hours	Lectures/ Tutorials/Practicals	
2.Student Learning Time:  100 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	300 hours	

<b>Module Title:</b> Control Engineering	<b>UEL Module Code:</b> EG5XXX  <b>ASU Module Code:</b> EG10534  <b>Level:</b> 5  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b> Dr. Mohamed Abouelatta
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To demonstrate knowledge and understanding of different control systems and control techniques.</li><li>• To select the appropriate controller for a given system and adjust its parameters.</li><li>• To demonstrate knowledge and understanding of control theory.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Open and closed loop control systems.</li><li>• Disturbance and parameters change.</li><li>• Transfer function and block diagram reduction.</li><li>• Time response and frequency response.</li><li>• Stability of control systems and Steady state errors.</li><li>• DC servo case study.</li><li>• PID controllers.</li><li>• Root Locus and Bode Plot.</li><li>• Lead and Lag compensators.</li><li>• Feedforward, Cascade and Ration Control.</li><li>• Computer based Control PLC and micro controllers.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Compare the different techniques for systems control;</li><li>2. Classify the systems with respect to their type and order.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>3. Employ alternative controllers as to get rid of noise and disturbance;</li><li>4. Design PID controllers and adjust their parameters.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>5. Design an entire control system;</li><li>6. Experiment and test developing software and hardware controllers.</li></ol> <p>Skills for life and work (general skills)</p>		

- |   |
|---|
| <ol style="list-style-type: none"> <li>7. Write control system characteristics;</li> <li>8. Develop software applications through MATLAB and C to control systems.</li> </ol> |
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<p><b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b></p>
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Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.

Assessment methods which enable students to demonstrate the learning outcomes for the module:	Weighting:	Learning Outcomes demonstrated
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**Portfolio**  
Continuous assessment tasks that include:  
Final Exam

Coursework divided as:

- Participation

- Lab reports

- Assignments
- Quizzes
- A midterm exam
- Final Exam

(105 hours)

1

100%	
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1. *Journal of the American Medical Association*, 1997; 277: 1033-1038.

**Reading and resources for the module:**

- Core**
- Kuo, B. C., & Golnaraghi, F. (2017) *Automatic control systems*. 10<sup>th</sup> edn. Englewood Cliffs, NJ: Prentice-Hall.

- ### Recommended
- Franklin, G. F., Powell, J. D., & Emami-Naeini, A. (2018) *Feedback control of dynamic systems*. 8<sup>th</sup> edn. Prentice Hall Press.
  - Ogata, K. (2009) *Modern Control Engineering*. 5<sup>th</sup> edn. Pearson.
  - Lavagno, L., Markov, I. L., Martin, G., & Scheffer, L. K. (Eds.). (2016) *Electronic Design Automation for IC Implementation, Circuit Design, and Process Technology: Circuit Design, and Process Technology*. 2<sup>nd</sup> edn. CRC Press.

Indicative Teaching and Learning Time (10 hrs per credit):	Activity
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1.Student/Tutor interaction,  
some of which may be online:

100 hours

Lectures/ Tutorials/ Practicals	
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2.Student Learning Time:

50 hours

Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.

Total hours (1 and 2):

150 hours
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<b>Module Title:</b> Satellite Communications	<b>UEL Module Code:</b> EG5XXX	<b>Module Leader:</b> Dr. Bassant Abdelhamid
	<b>ASU Module Code:</b> EG10535	
	<b>Level:</b> 5	
	<b>Credit:</b> 15	
	<b>ECTS credit:</b> 7.5	
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To build the student's knowledge in the fields of satellite communication.</li><li>• To improve the student's skills in the definition, analysis, and solving of problems related to satellite communication.</li><li>• To improve student's skills in the design of satellite communication subsystems.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Overview of satellite systems</li><li>• Satellite orbits and launching methods</li><li>• The Geostationary orbit</li><li>• The space segments</li><li>• Radio wave propagation</li><li>• Polarization of satellite signals</li><li>• Antennas for satellites and ground stations</li><li>• Space link budget</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Distinguish the space segment and the reliability of the satellite system modules;</li><li>2. Judge orbits, radio wave propagation, polarization, and antennas for satellite.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>3. Analyse a complete satellite system;</li><li>4. Design components and systems for specific applications related to satellite systems.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>5. Explain set up's preparation, use and maintenance;</li><li>6. Write Technical Reports.</li></ol> <p>Skills for life and work (general skills)</p> <ol style="list-style-type: none"><li>7. Work in a team and share ideas to achieve a task in a timely manner;</li><li>8. Communicate effectively.</li></ol>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>		

Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.

**Assessment methods which enable students to demonstrate the learning outcomes for the module:**

**Weighting:**

**Learning Outcomes demonstrated**

**Portfolio**

Continuous assessment tasks that include:

Final Exam

Coursework divided as:

- Participation
- Research Reports
- Assignments
- Quizzes
- A midterm exam
- Final Exam

(105 hours)

100%

1- 8

Reading and resources for the module:

**Core**

- Roody, D. (2006) *Satellite Communications*. 4<sup>th</sup> edn. McGraw-Hill.

**Recommended**

- Ippolito, L. J., & Ippolito Jr, L. J. (2017) *Satellite communications systems engineering: atmospheric effects, satellite link design and system performance*. 2<sup>nd</sup> edn. John Wiley & Sons.
- Maral, G. & Bousquet, M. (2009) *Satellite Communications Systems: Systems, Techniques and Technologies*. 5<sup>th</sup> edn. John Wiley and Sons Ltd.
- Maini, A. K., & Agrawal, V. (2014) *Satellite technology: principles and applications*. 3<sup>rd</sup> edn. John Wiley & Sons.
- Gagliardi, R. M. (2012) *Satellite communications*. 1<sup>st</sup> edn. Springer Science & Business Media.
- Kolawole, M. O. (2002) *Satellite communication engineering*. CRC Press.
- Mitra, M. (2005) *Satellite Communication*. PHI Learning Pvt. Ltd.

**Indicative Teaching and Learning Time (10 hrs per credit):**

**Activity**

1.Student/Tutor interaction, some of which may be online:

100 hours

Lectures/ Tutorials/Practicals

2.Student Learning Time:

50 hours

Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.

Total hours (1 and 2):

150 hours



<b>Module Title:</b> Statistical Signal Processing	<b>UEL Module Code:</b> EG5XXX  <b>ASU Module Code:</b> EG10536  <b>Level:</b> 5  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b> Dr. Bassant Abdelhamid
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To enrich the student knowledge about stochastic signals and stochastic processes.</li><li>• To develop the students' ability to develop algorithms for estimation, adaptive filtering, and detection.</li><li>• To train the students to use software packages to develop their own algorithms.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Statistical signal processing review.</li><li>• Discrete time random processes.</li><li>• Linear Algebra and error bounds.</li><li>• Least Square Method.</li><li>• Adaptive filters.</li><li>• Estimators.</li><li>• Introduction to detection.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Predict the statistical properties of random signals and processes;</li><li>2. Estimate the best estimator to minimize certain cost function.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>3. Analyse the type of random process according to its properties;</li><li>4. Solve linear systems using least square method.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>5. Design an adaptive filter in a certain application;</li><li>6. Use software package to design an estimator/detector for certain application.</li></ol> <p>Skills for life and work (general skills)</p> <ol style="list-style-type: none"><li>7. Effectively manage tasks, time;</li><li>8. Search for information and refer to relevant literatures.</li></ol>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b> <p>Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.</p>		

<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include: Final Exam Coursework divided as: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul> (105 hours)	<b>Weighting:</b>          100%	<b>Learning Outcomes demonstrated</b>          1- 8
Reading and resources for the module:  <b>Core</b> <ul style="list-style-type: none"> <li>- Manolakis, D. G., Ingle, V. K., &amp; Kogon, S. M. (2005) <i>Statistical and Adaptive Signal Processing</i>. Artech House. Inc., Norwood.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Meyer, C. D. (2000) <i>Matrix analysis and applied linear algebra</i> (Vol. 71). Siam.</li> <li>- Haykin, S. (1996) <i>Adaptive Filter Theory</i>. 3<sup>rd</sup> edn. Englewood Cliffs, NJ: Prentice-Hall.</li> <li>- Kay, S. M. (1993) <i>Fundamentals of statistical signal processing, volume I: Estimation theory</i>. Prentice Hall.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:   100 hours	   Lectures/ Tutorials/Practicals	
2.Student Learning Time:  50 hours	 Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	150 hours	

<b>Module Title:</b> VLSI Technology	<b>UEL Module Code:</b> EG5XXX  <b>ASU Module Code:</b> EG10537  <b>Level:</b> 5  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b> Dr. Sameh Ibrahim
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To build student background in issues related to electronic technology.</li><li>• To help students develop a thorough understanding of different integrated circuits fabrication processes, their environment, and their requirements.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Introduction to IC fabrication environment and clean rooms.</li><li>• The complete MOS transistor design flow.</li><li>• Ion Implantation.</li><li>• Diffusion.</li><li>• Lithography.</li><li>• Etching.</li><li>• Oxidation.</li><li>• Metallization and Epitaxy.</li><li>• Passives and Actives Layout.</li><li>• Layout Flow.</li><li>• Layout Techniques.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p><b>Knowledge</b></p> <ol style="list-style-type: none"><li>1. Differentiate the different processes required for integrated circuits fabrication;</li><li>2. Distinguish layout techniques, rules and guidelines.</li></ol> <p><b>Thinking skills</b></p> <ol style="list-style-type: none"><li>3. Evaluate the quality of integrated circuits design and fabrication;</li><li>4. Plan a transistor fabrication process from start to end.</li></ol> <p><b>Subject-based practical skills</b></p> <ol style="list-style-type: none"><li>5. Design a fabrication process with right parameters;</li><li>6. Develop an integrated circuit on layout level.</li></ol> <p><b>Skills for life and work (general skills)</b></p> <ol style="list-style-type: none"><li>7. Work in a group to achieve a task in a timely manner;</li><li>8. Refer to relevant literatures.</li></ol>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>		

Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.

**Assessment methods which enable students to demonstrate the learning outcomes for the module:**

**Weighting:**

**Learning Outcomes demonstrated**

**Portfolio**

Continuous assessment tasks that include:

Final Exam

Coursework divided as:

- Participation
- Research Reports
- Project
- Assignments
- Quizzes
- A midterm exam
- Final Exam

(105 hours)

100%

1- 8

Reading and resources for the module:

**Core**

- Campbell, S. (2013) *Fabrication Engineering at the Micro- and Nanoscale*. 4<sup>th</sup> edn. Oxford University Press.

**Recommended**

- Hastings, A. (2005) *The Art of Analog Layout*. 2<sup>nd</sup> edn. Pearson.

**Indicative Teaching and Learning Time (10 hrs per credit):**

**Activity**

1.Student/Tutor interaction, some of which may be online:

100 hours

Lectures/ Tutorials/Practicals

2.Student Learning Time:

50 hours

Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.

Total hours (1 and 2):

150 hours

<b>Module Title:</b> Introduction to Embedded Systems	<b>UEL Module Code:</b> EG5XXX  <b>ASU Module Code:</b> EG10538  <b>Level:</b> 5 <b>Credit:</b> 15 <b>ECTS credit:</b> 7.5	<b>Module Leader:</b>  Dr. Mohamed Abouelatta
<b>Pre-requisite:</b> None	<b>Pre-cursor:</b> None	
<b>Co-requisite:</b> None	<b>Excluded Combination:</b> None	
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To provide the technical background of embedded systems.</li><li>• To provide knowledge of different architectures of embedded systems.</li><li>• To train on design skills with embedded systems.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Embedded systems overview.</li><li>• The Embedded System Design Process.</li><li>• Formalisms for System Design, Case Study-1: Model Train Controller.</li><li>• Instruction Sets, ARM Processor and assembly.</li><li>• Programming Input and Output.</li><li>• Supervisor Mode, Exceptions, and Traps; Co-Processors.</li><li>• Memory System Mechanisms.</li><li>• Pipelining, Caching and power consumption Architecture.</li><li>• Bus-Based Computer Systems, DMA controller.</li><li>• I/O devices and Interfacing system design flow, codesign.</li><li>• Designing with Microprocessors, Development and Debugging.</li><li>• USB interfacing.</li><li>• Program Design and Analysis.</li><li>• Processes and Operating Systems.</li></ul>		
<b>Learning Outcomes for the Module</b> At the end of this module, students will be able to:  Knowledge <ul style="list-style-type: none"><li>1. Outline the architecture of embedded systems;</li><li>2. Compare embedded systems interfacing methods.</li></ul> Thinking skills <ul style="list-style-type: none"><li>3. Write metrics of embedded system design;</li><li>4. Analyse embedded system hardware and software.</li></ul> Subject-based practical skills <ul style="list-style-type: none"><li>5. Use hardware description of embedded systems circuits;</li><li>6. Design an embedded systems RTOS.</li></ul> Skills for life and work (general skills) <ul style="list-style-type: none"><li>7. Share system design skills;</li><li>8. Produce technical writing.</li></ul>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>		

Lectures/tutorials/practical sessions/videos. Feedback will be provided throughout the module in the form of both formative and summative work.

**Assessment methods which enable students to demonstrate the learning outcomes for the module:**

**Weighting:**

**Learning Outcomes demonstrated**

**Portfolio**

Continuous assessment tasks that include:

Final Exam

Coursework divided as:

- Participation
- Project
- Assignments
- Quizzes
- A midterm exam
- Final Exam

(105 hours)

100%

1- 8

**Reading and resources for the module:**

**Core**

- Wolf, M. (2012) *Computers as components: principles of embedded computing system design*. Elsevier.

**Recommended**

- Kopetz, H. (2011) *Real-time systems: design principles for distributed embedded applications*. Springer Science & Business Media.
- Lee, E. A., & Seshia, S. A. (2016) *Introduction to embedded systems: A cyber-physical systems approach*. 2<sup>nd</sup> edn. MIT Press.
- White, E. (2011) *Making Embedded Systems: Design Patterns for Great Software*. 1<sup>st</sup> edn. O'Reilly Media, Inc.
- Bai, Y. (2015) *Practical microcontroller engineering with ARM technology*. 1<sup>st</sup> edn. John Wiley & Sons.

**Indicative Teaching and Learning Time (10 hrs per credit):**

**Activity**

1.Student/Tutor interaction, some of which may be online:

100 hours

Lectures/ Tutorials/ Practical

2.Student Learning Time:

50 hours

Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.

Total hours (1 and 2):

150 hours

<b>Module Title:</b> Microwave Circuits	<b>UEL Module Code:</b> EG5XXX  <b>ASU Module Code:</b> EG10539  <b>Level:</b> 5  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b> Prof. Amr Safwat
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To enrich knowledge in the field of microwave and millimeter wave.</li><li>• To analyse the building blocks in microwave/millimeter wave circuits</li><li>• To develop the ability to use EM and circuit simulators.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Network theory.</li><li>• Planar transmission lines.</li><li>• Microwave components.</li><li>• Matching networks.</li><li>• Microwave Filters.</li><li>• Microwave Amplifiers.</li><li>• Microwave measurements.</li></ul>		
<b>Learning Outcomes for the Module</b> At the end of this module, students will be able to:  Knowledge <ul style="list-style-type: none"><li>1. Compare different configurations of transmission lines;</li><li>2. Interpret the operation of various microwave components.</li></ul> Thinking skills <ul style="list-style-type: none"><li>3. Design matching network;</li><li>4. Implement microwave amplifier.</li></ul> Subject-based practical skills <ul style="list-style-type: none"><li>5. Use microwave simulators to implement circuits in the available technology;</li><li>6. Measure the performance of the implemented microwave circuits.</li></ul> Skills for life and work (general skills) <ul style="list-style-type: none"><li>7. Work in a team;</li><li>8. Develop ideas and share then with others.</li></ul>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>  Lectures/tutorials/practical sessions/videos. Feedback will be provided throughout the module in the form of both formative and summative work.		

<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include: Final Exam Coursework divided as: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul> (105 hours)	<b>Weighting:</b>          100%	<b>Learning Outcomes demonstrated</b>          1- 8
<b>Reading and resources for the module:</b>  <b>Core</b> <ul style="list-style-type: none"> <li>- Pozar, D. M. (2011) <i>Microwave engineering</i>. 4<sup>th</sup> edn. John Wiley &amp; Sons.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Mongia, R. K., Hong, J., Bhartia, P., &amp; Bahl, I. J. (1999) <i>RF and microwave coupled-line circuits</i>. 2<sup>nd</sup> edn. Artech house.</li> <li>- Russer, P. (2006) <i>Electromagnetics, microwave circuit and antenna design for communications engineering</i>. 2<sup>nd</sup> edn. Artech House.</li> <li>- Montgomery, C. G., Dicke, R. H., &amp; Purcell, E. M. (Eds.) (1987) <i>Principles of microwave circuits</i> (No. 25). Iet.</li> <li>- Gupta, K. C., Garg, R., &amp; Chadha, R. (1981) <i>Computer aided design of microwave circuits</i>. NASA STI/Recon Technical Report A, 82.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:   100 hours	Lectures/ Tutorials/ Practical	
2.Student Learning Time:  50 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	150 hours	



<b>Module Title:</b> Optical Sensing	<b>UEL Module Code:</b> EG5XXX  <b>ASU Module Code:</b> EG105310  <b>Level:</b> 5  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b> Dr. Yasser Sabry Aboelmagd
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To recognize optical sensors and instruments.</li><li>• To understand the specifications and translate it to design requirements and practical aspects of the optical instruments and sensors.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Optical design.</li><li>• Interferometry.</li><li>• Spectroscopy.</li><li>• Polarization based instruments.</li><li>• Coherent imaging.</li></ul>		
<b>Learning Outcomes for the Module</b> At the end of this module, students will be able to:  Knowledge <ul style="list-style-type: none"><li>1. Differentiate current engineering technologies related to optical instrumentation;</li><li>2. Examine different techniques of material and structure characterization.</li></ul> Thinking skills <ul style="list-style-type: none"><li>3. Create methodical approaches when dealing with analysis of optical characterization problems;</li><li>4. Design characterization equipment combining components and concepts components.</li></ul> Subject-based practical skills <ul style="list-style-type: none"><li>5. Simulate optical sensing devices using CAD;</li><li>6. Read instruments manual and understand the specifications.</li></ul> Skills for life and work (general skills) <ul style="list-style-type: none"><li>7. Demonstrate efficient IT capabilities;</li><li>8. Undertake independent research.</li></ul>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>  Lectures/tutorials/practical sessions/videos. Feedback will be provided throughout the module in the form of both formative and summative work.		

<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include: Final Exam Coursework divided as: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab reports</li> <li>• practical demonstrations</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul> (105 hours)	<b>Weighting:</b>          100%	<b>Learning Outcomes demonstrated</b>          1- 8
<b>Reading and resources for the module:</b>  <b>Core</b> <ul style="list-style-type: none"> <li>- Nagabhushana, S. (2010) <i>Lasers and optical instrumentation</i>. 10<sup>th</sup> edn. IK International Pvt Ltd.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Goodwin, E. P., &amp; Wyant, J. C. (2006) <i>Field guide to interferometric optical testing</i>. Bellingham, WA: SPIE.</li> <li>- Udd, E., &amp; Spillman, W. B. (2014) <i>Field guide to fiber optic sensors</i>. Proc. SPIE.</li> <li>- Gåsvik, K. J. (2003) <i>Optical metrology</i>. 3rd edn. John Wiley &amp; Sons.</li> <li>- Griffiths, P. R., &amp; De Haseth, J. A. (2007) <i>Fourier transform infrared spectrometry</i>. 2nd edn. John Wiley &amp; Sons.</li> <li>- Fujimoto, J. G., &amp; Farkas, D. (2009) <i>Biomedical optical imaging</i>. 1<sup>st</sup> edn. Oxford University Press.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:  100 hours	Lectures/ Tutorials/ Practical	
2.Student Learning Time:  50 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	150 hours	

<b>Module Title:</b> Advanced Communication Networks	<b>UEL Module Code:</b> EG6XXX  <b>ASU Module Code:</b> EG10641  <b>Level:</b> 6  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b> Dr. Hussein Abdel Atty Elsayed
<b>Pre-requisite:</b> None	<b>Pre-cursor:</b> None	
<b>Co-requisite:</b> None	<b>Excluded Combination:</b> None	
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To understand the terminologies and elements of computer networks.</li><li>• To understand the operation of some computer networks.</li><li>• To illustrate network addressing.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Network architecture.</li><li>• TCP/IP model.</li><li>• Network topology.</li><li>• Physical layer.</li><li>• Data link protocols.</li><li>• Network layer protocols.</li><li>• Local, area networks.</li><li>• Transport layer protocols.</li><li>• VoIP applications.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Distinguish the different type of networks;</li><li>2. Interpret the operation of Ethernet networks and IP, TCP and UDP protocols.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>3. Design IP address subnetting;</li><li>4. Interpret the performance of different protocols.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>5. Differentiate congestion control techniques;</li><li>6. Design IP address subnet.</li></ol> <p>Skills for life and work (general skills)</p> <ol style="list-style-type: none"><li>7. Work in a team;</li><li>8. Formulate computer network problems.</li></ol>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>		

Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.		
<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include: Final Exam Coursework divided as: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab reports</li> <li>• Fieldwork reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul> (105 hours)	<b>Weighting:</b>          100%	<b>Learning Outcomes demonstrated</b>          1- 8
Reading and resources for the module:  <b>Core</b> <ul style="list-style-type: none"> <li>- Tanenbaum, A. S. (2008) <i>Computer Networks</i>. 6<sup>th</sup> edn. Prentice Hall.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Stallings, W. (2013) <i>Data and computer communications</i>. 10<sup>th</sup> edn. Prentice Hall.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>  1.Student/Tutor interaction, some of which may be online:   100 hours	<b>Activity</b>       Lectures/ Tutorials/Practicals	
2.Student Learning Time:   50 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	150 hours	

<b>Module Title:</b> Graduation Project	<b>UEL Module Code:</b> EG6XXX  <b>ASU Module Code:</b> EG10642  <b>Level:</b> 6  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b> Dr. Hussein Abdel Atty Elsayed
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• Teach students the analysis and design of a complete engineering system using the fundamentals, principles, and skills gained during their study.</li><li>• Increase the student capability in technical report writing and presentation in engineering problems.</li></ul>		
<b>Main Topics of Study:</b> <p>A single or group project performed under the supervision of a faculty member and an industrial entity. The main topics of study:</p> <ul style="list-style-type: none"><li>• Project Overview and Understanding</li><li>• Identification of a suitable project topic</li><li>• Research methods</li><li>• Project Design</li><li>• Project Implementation</li><li>• Project testing, validation, and verification</li><li>• Project Documentation and Presentation</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Conduct a literature review in the project domain;</li><li>2. Collect data of the current engineering problems in industry related to the graduation project topic;</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>3. Select appropriate analytical methods, tools, and computer software for a specific communication system problem;</li><li>4. Design an engineering system for a specific application.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>5. Solve engineering problems and implement solutions</li><li>6. Test and verify the implemented system.</li></ol> <p>Skills for life and work (general skills)</p> <ol style="list-style-type: none"><li>7. Work in a team and share ideas to achieve a task in a timely manner;</li><li>8. Present reports, discuss results and defend ideas of experiments.</li></ol>		

<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>  Lectures will be used to provide an overview of the project, its requirements and organisation and an introduction to research methods, literature surveys and referencing. In addition, every student will be allocated a supervisor at an early stage within the module. The supervisor will support the student for the duration of the project. Feedback, in the form of formative assessment, will be provided by supervisors on a regular basis.		
<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include:  Portfolio 1 (105 hours)  Portfolio 2 (105 hours)  Each portfolio includes 8000-word project report, intermediate deliverables and a 15-minute viva presentation.	<b>Weighting:</b>  50%  50%	<b>Learning Outcomes demonstrated</b>  1-3, 5, 7, 8  4, 5-8
Reading and resources for the module:  <b>Core</b> <ul style="list-style-type: none"> <li>- Jagodzinski, P., Reid, F. J. M., Culverhouse, P., Parsons, R., &amp; Phillips, I. (2000) <i>A study of electronics engineering design teams</i>. Design Studies, 21(4), 375-402.</li> <li>- Brinkman, G. W., &amp; van der Geest, T. M. (2003) <i>Assessment of communication competencies in engineering design projects</i>. Technical communication quarterly, 12(1), 67-81.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Bartol, K, Martin, D, Tein, M &amp; Matthews, G (2011) <i>Management: a pacific RIM focus</i>. 6<sup>th</sup> edn, McGraw-Hill, North Ryde, NSW.</li> <li>- Yong-chun, L. X. M. Z. (2004) <i>Study and Practice of Quality Control on Graduation Project [J]</i>. Journal of Nanjing University of Science and Technology, 2.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:  100 hours	Lectures/ Supervision/ Practicals	
2.Student Learning Time:  200 hours	Essential and background reading, tutorial preparation, assignment planning and preparation.	
Total hours (1 and 2):	300 hours	

<b>Module Title:</b> Entrepreneurship	<b>UEL Module Code:</b> EG6XXX  <b>ASU Module Code:</b> EG10643  <b>Level:</b> 6  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b> P. Amr Safwat
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To improve the professional skills in establishing business models.</li><li>• To write a small business plan and effectively manage tasks, time, and resources.</li><li>• To use available project management software.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Product development and idea protection: NASA Technology readiness level and product life cycle.</li><li>• Intellectual property: patent, claims, protection, national phase, international patent application.</li><li>• Funding: Government funding versus venture capital.</li><li>• Finance: Basic Accounting Equations.</li><li>• Finance: Financial statements.</li><li>• Finance: Adjustments.</li><li>• Finance: Worksheet.</li><li>• Corporate and stock market growth versus dilution.</li><li>• Project management: work breakdown structure.</li><li>• Project management: time flow, critical paths, slack.</li><li>• Project management: Gantt Chart.</li><li>• Business model Canvas.</li><li>• Business plan: return of investment, Net present value.</li><li>• Pitching strategy.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Prepare the four financial statements;</li><li>2. Examine Project life cycle;</li><li>3. Develop Gantt and PERT charts.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>4. Analyse financial statements;</li><li>5. Develop Plans for managing a project efficiently.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>6. Demonstrate organizational and project management skills;</li><li>7. Write a small business plan.</li></ol>		

Skills for life and work (general skills)		
8. Effectively manage tasks, time, and resources.		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>		
Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.		
<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>	<b>Weighting:</b>	<b>Learning Outcomes demonstrated</b>
<b>Portfolio</b> Continuous assessment tasks that include: Final Exam Coursework divided as: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Project</li> <li>• Fieldwork reports</li> <li>• Oral presentations</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul> (105 hours)	100%	1- 8
Reading and resources for the module:		
<b>Core</b> <ul style="list-style-type: none"> <li>- Larson, E., and Gray, C. (2014) <i>Project Management</i>. McGraw-Hill.</li> </ul>		
<b>Recommended</b> <ul style="list-style-type: none"> <li>- Weygandt, J., Kimmel, P., and Kieso, D. (2015) <i>Accounting Principles</i>. 12<sup>nd</sup> edn John-Wiley &amp; Sons.</li> <li>- Bravo, M. (2012), <i>Dream, Design, Surf How to design new ventures for rapid growth, lower risk and global impact</i>. Academy Press.</li> <li>- Kerzener, H. (2013) <i>Project Management: A system Approach to Planning, Scheduling, and control</i>. 13<sup>rd</sup> edn. John Wiley &amp; Sons, Inc.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:		
100 hours	Lectures/ Tutorials/Practicals	
2.Student Learning Time:		
50 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	150 hours	



<b>Module Title:</b> Information Theory and Coding	<b>UEL Module Code:</b> EG6XXX  <b>ASU Module Code:</b> EG10644  <b>Level:</b> 6  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b> Dr. Bassant Abdelhamid
<b>Pre-requisite:</b> None	<b>Pre-cursor:</b> None	
<b>Co-requisite:</b> None	<b>Excluded Combination:</b> None	
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To explain the concept of information theory.</li><li>• To design different channel encoders (linear block encoders and convolution encoders).</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Uncertainty, Information, Entropy and its properties.</li><li>• Source coding: Prefix coding, First Shannon theorem, Huffman coding.</li><li>• Discrete memoryless channels, Binary symmetric channel, Mutual information and its properties. Channel capacity.</li><li>• Channel coding, Second Shannon theorem, Mutual information. Channel capacity, Compression of information.</li><li>• Linear block codes, cyclic codes, Well-Known Block codes.</li><li>• Convolution codes: Code tree, Trellis and state diagram. Maximum likelihood decoding of convolution codes.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ol style="list-style-type: none"><li>1. Distinguish the difference between source coding and channel coding;</li><li>2. Compare the linear block and the convolution codes.</li></ol> <p>Thinking skills</p> <ol style="list-style-type: none"><li>3. Differentiate discrete memoryless sources;</li><li>4. Analyse channel encoder and decoder.</li></ol> <p>Subject-based practical skills</p> <ol style="list-style-type: none"><li>5. Apply the coding and decoding algorithms in a practical application;</li><li>6. Design channel coder.</li></ol> <p>Skills for life and work (general skills)</p> <ol style="list-style-type: none"><li>7. Work in a team and share ideas;</li><li>8. Communicate effectively.</li></ol>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b> <p>Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.</p>		

<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include: Final Exam Coursework divided as: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul> (105 hours)	<b>Weighting:</b>          100%	<b>Learning Outcomes demonstrated</b>          1- 8
Reading and resources for the module:  <b>Core</b> <ul style="list-style-type: none"> <li>- Haykin, S. (2009) <i>Communication Systems</i>. 5<sup>th</sup> edn. John Wiley &amp; Sons.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Praxis, J. &amp; Salehi, M. (2008) <i>Digital Communications</i>. 5<sup>th</sup> edn. McGraw-Hill.</li> <li>- Cover, T. M., &amp; Thomas, J. A. (2012) <i>Elements of information theory</i>. 3<sup>rd</sup> edn. John Wiley &amp; Sons.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:   100 hours	Lectures/ Tutorials/Practicals	
2.Student Learning Time:  50 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	150 hours	

<b>Module Title:</b> Wireless and Mobile Communications	<b>UEL Module Code:</b> EG6XXX	<b>Module Leader:</b> Dr. Bassant Abdelhamid
	<b>ASU Module Code:</b> EG10645	
	<b>Level:</b> 6	
	<b>Credit:</b> 30	
	<b>ECTS credit:</b> 15	
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To describe frequency planning.</li><li>• To explain propagation models.</li><li>• To study wireless and mobile standard systems as: GSM, CDMA, 3G, 4G and 5G.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Review of Frequency planning.</li><li>• Multipath wireless channel.</li><li>• Propagation models (small and large scale).</li><li>• GSM system.</li><li>• CDMA system.</li><li>• 3G, 4G and 5G system.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p>Knowledge</p> <ul style="list-style-type: none"><li>1. Plan frequency planning steps;</li><li>2. Distinguish GSM and CDMA mobile system standard;</li><li>3. Compare 3G, 4G and 5G mobile systems.</li></ul> <p>Thinking skills</p> <ul style="list-style-type: none"><li>4. Analyse cellular system;</li><li>5. Design GSM system.</li></ul> <p>Subject-based practical skills</p> <ul style="list-style-type: none"><li>6. Create a mobile system.</li></ul> <p>Skills for life and work (general skills)</p> <ul style="list-style-type: none"><li>7. Collaborate effectively within multidisciplinary team;</li><li>8. Undertake independent research.</li></ul>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b> <p>Lectures/tutorials/practical sessions/workshops. Feedback will be provided throughout the module in the form of both formative and summative work.</p>		

<p><b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b></p> <p><b>Portfolio 1 (105 hours)</b></p> <p><b>Portfolio 2 (105 hours)</b></p> <p>Each portfolio in the module contains:</p> <ul style="list-style-type: none"> <li>• Participation</li> <li>• Fieldwork reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul> <p>(210 hours)</p>	<p><b>Weighting:</b></p> <p>50%</p> <p>50%</p>	<p><b>Learning Outcomes demonstrated</b></p> <p>1, 2, 7</p> <p>3, 4, 5-6,8</p>
<p>Reading and resources for the module:</p> <p><b>Core</b></p> <ul style="list-style-type: none"> <li>- Rappaport, T. S. (2001) <i>Wireless Communication System</i>. 2nd edn. New Jersey: prentice hall PTR.</li> <li>- Lee, W. C. (1982) <i>Mobile communications engineering</i>. McGraw-Hill Professional.</li> </ul> <p><b>Recommended</b></p> <ul style="list-style-type: none"> <li>- Nee, R. V., &amp; Prasad, R. (2000) <i>OFDM for wireless multimedia communications</i>. Artech House, Inc.</li> <li>- Lee, W. C. (2010) <i>Mobile communications design fundamentals</i>. (Vol. 25). John Wiley &amp; Sons.</li> <li>- Rappaport, T. S. (1996) <i>Wireless communications: principles and practice</i>. (Vol. 2). New Jersey: prentice hall PTR.</li> </ul>		
<p><b>Indicative Teaching and Learning Time (10 hrs per credit):</b></p>	<p><b>Activity</b></p>	
<p>1.Student/Tutor interaction, some of which may be online:</p> <p>200 hours</p>	<p>Lectures/ Tutorials/Practicals</p>	
<p>2.Student Learning Time:</p> <p>100 hours</p>	<p>Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.</p>	
<p>Total hours (1 and 2):</p>	<p>300 hours</p>	

<b>Module Title:</b> Processing for Multimedia	<b>UEL Module Code:</b> EG6XXX  <b>ASU Module Code:</b> EG10646  <b>Level:</b> 6  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b> Dr. Bassant Abdelhamid
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To understand the theory and algorithms that are widely used in digital image processing.</li><li>• To understand current technologies and issues that are specific to image processing systems.</li><li>• To apply critical thinking about shortcomings of the state of the art in image processing.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Image Enhancement: Spatial Domain.</li><li>• Image Enhancement: frequency Domain.</li><li>• Image Restoration: Noise Removal.</li><li>• Image Restoration: Degradation Model.</li><li>• Image Compression: Mathematical Methods.</li><li>• Image Compression: JPEG Compression.</li></ul>		
<b>Learning Outcomes for the Module</b> At the end of this module, students will be able to:  Knowledge <ul style="list-style-type: none"><li>1. Distinguish the bases of digital image formation;</li><li>2. Deduce the essential mathematics relevant to image compression, image enhancement and restoration.</li></ul> Thinking skills <ul style="list-style-type: none"><li>3. Discriminate between spatial filters for image enhancement;</li><li>4. Demonstrate alternative methods for image restoration techniques and image compression techniques.</li></ul> Subject-based practical skills <ul style="list-style-type: none"><li>5. Use image processing toolboxes and other related software;</li><li>6. Implement image processing techniques to real-world applications.</li></ul> Skills for life and work (general skills) <ul style="list-style-type: none"><li>7. Present reports, discuss results and defend their ideas of experiments;</li><li>8. Communicate effectively and share ideas ethically in a team work.</li></ul>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>		

Lectures/tutorials/practical sessions/videos. Feedback will be provided throughout the module in the form of both formative and summative work.		
<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include: Final Exam Coursework divided as: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Oral presentations</li> <li>• Fieldwork reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul> (105 hours)	<b>Weighting:</b>          100%	<b>Learning Outcomes demonstrated</b>          1- 8
<b>Reading and resources for the module:</b>  <b>Core</b> <ul style="list-style-type: none"> <li>- Gonzalez, R. C., &amp; Woods, R. E. (2007) <i>Digital image processing</i>. 3<sup>rd</sup> edn. Prentice Hall.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Sayood, K. (2005) <i>Introduction to Data Compression</i>. 3<sup>rd</sup> edn. Morgan Kaufmann.</li> <li>- Szeliski, R. (2010) <i>Computer vision: algorithms and applications</i>. Springer Science &amp; Business Media.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>  1.Student/Tutor interaction, some of which may be online:   100 hours	<b>Activity</b>       Lectures/ Tutorials/ Practical	
2.Student Learning Time:   50 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	150 hours	

<b>Module Title:</b> Analog and RF Circuit Design	<b>UEL Module Code:</b> EG6XXX  <b>ASU Module Code:</b> EG10647  <b>Level:</b> 6  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b> Dr. Hesham Omran
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To apply analytical skills that can be used in the field of analog circuit design.</li><li>• To apply intuition as to quickly assess analog and RF circuit performance and functionality.</li><li>• To apply analytical skills that can be used in the field of RF circuit design.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Review of transistor models and amplifier circuits.</li><li>• Advanced current mirrors.</li><li>• Operational amplifiers design (basic, two-stage, telescopic, folded).</li><li>• Stability and frequency compensation.</li><li>• Fully differential operation and common-mode feedback circuits.</li><li>• Voltage and current references.</li><li>• Noise and mismatch in analog circuits.</li><li>• Basic concepts of RF circuits and systems.</li><li>• RF transceivers architectures.</li><li>• Noise and distortion in RF systems.</li><li>• Impedance matching and smith chart.</li><li>• Building blocks of RF systems: Low noise amplifiers, mixers, oscillators, phase noise.</li><li>• RF frequency synthesizers.</li><li>• RF power amplifiers.</li></ul>		
<b>Learning Outcomes for the Module</b> At the end of this module, students will be able to:  Knowledge <ul style="list-style-type: none"><li>1. Simulate the characteristics of analog integrated circuits;</li><li>2. Distinguish analog and RF circuits noise sources and performance;</li><li>3. Examine the specifications of RF circuit blocks.</li></ul> Thinking skills <ul style="list-style-type: none"><li>4. Implement advanced analog integrated circuits;</li><li>5. Analyse RF circuit blocks (LNA, mixer, oscillator).</li></ul> Subject-based practical skills <ul style="list-style-type: none"><li>6. Design analog and RF circuits using simulation tools;</li></ul>		

<p>7. Create technical reports.</p> <p>Skills for life and work (general skills)</p> <p>8. Work in teams.</p>		
<p><b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b></p> <p>Lectures/tutorials/practical sessions/videos. Feedback will be provided throughout the module in the form of both formative and summative work.</p>		
<p><b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b></p> <p><b>Portfolio</b> Continuous assessment tasks that include:</p> <p>Portfolio 1 (105 hours)</p> <p>Portfolio 2 (105 hours)</p> <p>Each portfolio in the module contains:</p> <ul style="list-style-type: none"> <li>• Participation</li> <li>• Fieldwork reports</li> <li>• Project</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>	<p><b>Weighting:</b></p> <p>50%</p> <p>50%</p>	<p><b>Learning Outcomes demonstrated</b></p> <p>1, 4, 6-8</p> <p>2,3, 5-8</p>
<p><b>Reading and resources for the module:</b></p> <p><b>Core</b></p> <ul style="list-style-type: none"> <li>- Razavi, B. (2017) <i>Design of analog CMOS integrated circuits</i>. 2<sup>nd</sup> edn. McGraw-Hill.</li> <li>- Razavi, B. (2011) <i>RF Microelectronics</i>. 2<sup>nd</sup> edn. Prentice Hall.</li> </ul> <p><b>Recommended</b></p> <ul style="list-style-type: none"> <li>- Johns, D.A. &amp; Martin, K. (2011) <i>Analog integrated circuit design</i>. 2<sup>nd</sup> edn. John Wiley &amp; Sons.</li> <li>- Sansen, W. M. (2007) <i>Analog design essentials</i>. Springer Science &amp; Business Media.</li> <li>- Jespers, P. G., &amp; Murmann, B. (2017) <i>Systematic Design of Analog CMOS Circuits</i>. Cambridge University Press.</li> </ul>		
<p><b>Indicative Teaching and Learning Time (10 hrs per credit):</b></p> <p>1.Student/Tutor interaction, some of which may be online:</p> <p>200 hours</p>	<p><b>Activity</b></p> <p>Lectures/ Tutorials/ Practical</p>	
<p>2.Student Learning Time:</p> <p>100 hours</p>	<p>Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.</p>	
<p>Total hours (1 and 2):</p>	<p>300 hours</p>	



<b>Module Title:</b> ASIC Design & Automation	<b>UEL Module Code:</b> EG6XXX  <b>ASU Module Code:</b> EG10648  <b>Level:</b> 6  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b> Dr. Sameh Ibrahim
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To enrich the student’s knowledge in designing application specific digital integrated circuits.</li><li>• To enrich the student’s knowledge in fully-automated digital design flow.</li><li>• To enhance the student ability to use integrated circuit computer-aided design and verification tools.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• IC Subsystems.</li><li>• Standard-cell design.</li><li>• Digital Flow – High-Level Design.</li><li>• VHDL – Basics.</li><li>• VHDL - Combinational Design.</li><li>• VHDL - Sequential Design.</li><li>• CAD System.</li><li>• Simulation Flow.</li><li>• Finite-State Machines.</li><li>• Logic Synthesis - Two-level.</li><li>• Logic Synthesis - Multi-level.</li><li>• Design for Test.</li><li>• Placement.</li><li>• Routing.</li><li>• Chip Integration.</li></ul>		
<b>Learning Outcomes for the Module</b> <p>At the end of this module, students will be able to:</p> <p><b>Knowledge</b></p> <ol style="list-style-type: none"><li>1. Compare digital design and verification flows;</li><li>2. Distinguish basic hardware description language HDL (VHDL/Verilog);</li><li>3. Evaluate different integrated circuit test strategies.</li></ol> <p><b>Thinking skills</b></p> <ol style="list-style-type: none"><li>4. Create a state-machine from a problem description;</li><li>5. Compile a synthesizable HDL code.</li></ol> <p><b>Subject-based practical skills</b></p> <ol style="list-style-type: none"><li>6. Isolate design challenges during digital design process;</li><li>7. Build a simple chip from specifications down to layout using CAD tools.</li></ol>		



<b>Module Title:</b> Microwave Systems	<b>UEL Module Code:</b> EG6XXX  <b>ASU Module Code:</b> EG10649  <b>Level:</b> 6  <b>Credit:</b> 30  <b>ECTS credit:</b> 15	<b>Module Leader:</b> Prof. Amr Safwat
<b>Pre-requisite:</b> None		<b>Pre-cursor:</b> None
<b>Co-requisite:</b> None		<b>Excluded Combination:</b> None
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To provide knowledge and understanding of microwave tubes and semiconductor devices</li><li>• To perform testing measurements in microwave domain.</li><li>• To enrich the student's knowledge in handling and dealing with microwave technology including installation of components, devices, and systems.</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Two-cavity Klystron.</li><li>• Reflex Klystron.</li><li>• Travelling wave amplifier.</li><li>• Magnetron.</li><li>• Microwave Transistors (BJT, HBT, FET, HEMT).</li><li>• Negative resistance devices (Tunnel diodes, Gunn diodes and avalanche diode).</li><li>• Parametric amplifiers.</li><li>• Microwave Cavities.</li><li>• Planar transmission lines characterization.</li><li>• Injection phase locking of microwave oscillator.</li><li>• Antennas characterization.</li><li>• CAD and analysis of microstrip discontinuities, couplers, and antennas.</li></ul>		
<b>Learning Outcomes for the Module</b> At the end of this module, students will be able to:  Knowledge <ul style="list-style-type: none"><li>1. Compare solid knowledge of structure and operation of Klystrons, Magnetrons and travelling wave tubes;</li><li>2. Distinguish microwave transistors and their use as amplifiers and oscillators.</li></ul> Thinking skills <ul style="list-style-type: none"><li>3. Analyse and design microwave oscillators;</li><li>4. Select appropriate amplifiers and oscillators for microwave systems and subsystems.</li></ul> Subject-based practical skills <ul style="list-style-type: none"><li>5. Use software package to design different microwave components;</li><li>6. Perform experiments in the microwave domain.</li></ul>		

Skills for life and work (general skills) 7. Work in a team and share ideas to achieve a task in a timely manner; 8. Develop ideas and share with others.		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>  Lectures/tutorials/practical sessions/videos. Feedback will be provided throughout the module in the form of both formative and summative work.		
<b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b>  <b>Portfolio</b> Continuous assessment tasks that include:  Portfolio 1 (105 hours)  Portfolio 2 (105 hours)  Each portfolio in the module contains: <ul style="list-style-type: none"> <li>• Participation</li> <li>• Fieldwork reports</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul>	<b>Weighting:</b>          50%          50%	<b>Learning Outcomes demonstrated</b>          1-4          5-8
<b>Reading and resources for the module:</b>  <b>Core</b> <ul style="list-style-type: none"> <li>- Liao, S. Y. (2003) <i>Microwave devices and circuits</i>. 3rd edn. Pearson Education India.</li> <li>- Pozar, D. M. (2011) <i>Microwave engineering</i>. 4<sup>th</sup> edn. John Wiley &amp; Sons.</li> </ul> <b>Recommended</b> <ul style="list-style-type: none"> <li>- Gupta, K. C., Garg, R., Bahl, I. J., &amp; Bhartia, P. (2013) <i>Microstrip Lines and Slot lines</i>. 3rd edn. Artech House.</li> </ul>		
<b>Indicative Teaching and Learning Time (10 hrs per credit):</b>	<b>Activity</b>	
1.Student/Tutor interaction, some of which may be online:  200 hours	Lectures/ Tutorials/ Practical	
2.Student Learning Time:  100 hours	Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.	
Total hours (1 and 2):	300 hours	

<b>Module Title:</b> Optical Communications	<b>UEL Module Code:</b> EG6XXX  <b>ASU Module Code:</b> EG106410  <b>Level:</b> 6  <b>Credit:</b> 15  <b>ECTS credit:</b> 7.5	<b>Module Leader:</b> Dr. Yasser Sabry Aboelmagd
<b>Pre-requisite:</b> None	<b>Pre-cursor:</b> None	
<b>Co-requisite:</b> None	<b>Excluded Combination:</b> None	
<b>Locations of delivery:</b> ASU		
<b>Main Aim(s) of the Module:</b> <ul style="list-style-type: none"><li>• To distinguish between the various modes of operation of optical fibers</li><li>• To identify the various causes for signal degradation including losses and dispersion</li><li>• To analyse and design optical components for communication applications</li></ul>		
<b>Main Topics of Study:</b> <ul style="list-style-type: none"><li>• Optical fibers.</li><li>• Semiconductor lasers.</li><li>• Optical components.</li><li>• Optical modulation.</li><li>• Optical amplifiers.</li></ul>		
<b>Learning Outcomes for the Module</b> At the end of this module, students will be able to:  Knowledge <ul style="list-style-type: none"><li>1. Express the guided-wave phenomena and light-matter interactions and their engineering applications;</li><li>2. Distinguish the fiber systems and their applications in optical communications.</li></ul> Thinking skills <ul style="list-style-type: none"><li>3. Develop analytical models for optical communication devices and systems;</li><li>4. Design of optical components and systems for specific applications.</li></ul> Subject-based practical skills <ul style="list-style-type: none"><li>5. Create fiber connectorization and splicing;</li><li>6. Perform optical components characterization.</li></ul> Skills for life and work (general skills) <ul style="list-style-type: none"><li>7. Work in a team to achieve a task in a timely manner;</li><li>8. Develop and share ideas.</li></ul>		
<b>Teaching/ learning methods/strategies used to enable the achievement of learning outcomes:</b>  Lectures/tutorials/practical sessions/videos. Feedback will be provided throughout the module in the form of both formative and summative work.		

<p><b>Assessment methods which enable students to demonstrate the learning outcomes for the module:</b></p> <p><b>Portfolio</b>  Continuous assessment tasks that include:  Final Exam  Coursework divided as:</p> <ul style="list-style-type: none"> <li>• Participation</li> <li>• Lab reports</li> <li>• Practical demonstrations</li> <li>• Assignments</li> <li>• Quizzes</li> <li>• A midterm exam</li> <li>• Final Exam</li> </ul> <p>(105 hours)</p>	<p><b>Weighting:</b></p> <p>100%</p>	<p><b>Learning Outcomes demonstrated</b></p> <p>1- 8</p>
<p><b>Reading and resources for the module:</b></p> <p><b>Core</b></p> <ul style="list-style-type: none"> <li>- Kumar, S. &amp; Jamal Deen, M. (2014) <i>Fiber Optic Communications: Fundamentals and applications</i>. 1<sup>st</sup> edn. John Wiley and Sons.</li> </ul> <p><b>Recommended</b></p> <ul style="list-style-type: none"> <li>- Papen, G. C. &amp; Blahut, R. E. (2019) <i>Lightwave Communications</i>. 1<sup>st</sup> edn. Cambridge University Press.</li> <li>- Chrostowski, L., &amp; Hochberg, M. (2015) <i>Silicon photonics design: from devices to systems</i>. 1<sup>st</sup> edn. Cambridge University Press.</li> <li>- Agrawal, G. P. (2012) <i>Fiber-optic communication systems</i>. John Wiley &amp; Sons.</li> <li>- Solgaard, O. (2009) <i>Photonic microsystems: Micro and nanotechnology applied to optical devices and systems</i>. Springer Science &amp; Business Media.</li> <li>- Yariv, A. &amp; Yeh, P. (2007) <i>Photonics: Optical Electronics in modern communications</i>. 6<sup>th</sup> edn. Oxford University Press.</li> </ul>		
<p><b>Indicative Teaching and Learning Time (10 hrs per credit):</b></p>	<p><b>Activity</b></p>	
<p>1.Student/Tutor interaction, some of which may be online:</p> <p>100 hours</p>	<p>Lectures/ Tutorials/ Practical</p>	
<p>2.Student Learning Time:</p> <p>50 hours</p>	<p>Essential and background reading, tutorial preparation, assignment planning and preparation, examination revision.</p>	
<p>Total hours (1 and 2):</p>	<p>150 hours</p>	

## 7 PLACEMENT REQUIREMENTS

Although there is no compulsory placement system, we encourage all students to seek work experience during their summer vacations. Training could be performed in an industrial/service facility related to the student's program and must be under the full supervision of the faculty according to the requirements stipulated in Article (37) of the ASU Credit-hour Educational Programmes bylaws. The training is mandatory for the normal ASU degree.

### **Scholarships**

The student who achieves an accumulative GPA of 3.6 or higher after any semester and did not fail any course throughout his course of study is included in the Dean's List and receives partial exemption from charges on the next semester. This exemption is dependent on the student's GPA as recommended by the Programmes Administration Council in this regard and after approval of the Council of the Faculty of Engineering. The student who keeps an accumulative GPA of 3.3 or higher in every semester all through his course of study and does not fail any course, graduates with an Honor Degree, which is documented in his graduation certificate. Additionally, the top 30 students in Thanaweya Amma, mathematics section, who enrolled in the credit hours programmes, are fully exempted from paying any tuition fees in their first semester.

To maintain this exemption in the following semesters, the student should maintain an accumulative GPA of 3.6 or higher in every semester. This exemption is declined once the student fails to achieve this accumulative GPA in any semester. The faculty sets a system for encouraging distinguished students through reducing their tuition fees in accordance with their accumulative GPAs. At the beginning of each semester, the distinguished students' list is announced together with the associated tuition fees reductions.

Reference to the Suitability procedure and provide web link

***<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Manual-of-General-Regulations>***  
(Manual of General Regulations, Part 13)

## **8      PROGRAMME MANAGEMENT**

- Students' support and guidance are provided through a range of resources. A welcome and induction process is delivered in their first week, where all students are guided to their programme studies.
- The programme pays special attention to the learning management system that helps students and staff members to intercommunicate effectively in terms of course material, assignment, term-work marks ... etc.
- The programme's learning management system is setup to have a page for each course studied during the semester. The student can access courses from the main programme web-page.
- All electronic services provided to the students requires the use of university e-mail, hence, it is created automatically for the programme's student when first enrolled to the programme, and this e-mail is retained until graduation.
- The Student Information System (SIS) is the place where students can access all your academic records. It can be reached on the main programme web-page, which also provides brief information about the mission and vision of the programme, and the important dates related to student academic activities.
- Every student is assigned an Academic Advisor who is one of the faculty members and may continue with the student for the whole study duration. The Academic Advisor should follow-up with the student, assist in selecting courses each semester, and request to place the student under probation for one semester.
- For each hour (lectures or tutorials) the instructor should have an office hour. It could be twice a week for 1.5 hours each. Office hours will be determined in the first class and will be posted on the Instructor's office door.
- Students will be given a student handbook at the start of their programme of study.
- Programme Committees provide a formal structure for student participation and feedback on their programme of study. Programme committees provide a forum in which students can express their views about the management of the programme, and the content, delivery and assessment of modules, in order to identify appropriate actions to be taken. Terms of reference are provided in Appendix D.



## **Students Involvement**

There are different facilities that ensure students involvement that include:

### **a) Students' Affairs Administration**

The students' affairs administration is chaired by the Vice-Dean for education and students' affairs and is located in the main building. This administration has representatives at the programmes' administration offices (Ground Floor of the New Educational Building). The secretariat of each programme (at the programmes secretariat office – Ground Floor of the New Educational Building) also collaborates with the previous representatives in accomplishing the following tasks:

- Archiving of the students' files.
- Issuing the students' identity cards.
- Electronic recording of the students' course registration, add/drop, and withdraw.
- Processing the students' course evaluation at the end of each semester.
- Issuing the students' records at the end of each semester.
- Issuing the students' graduation certificates.
- Processing the students' appeals and requests.

### **b) Students' Union**

The students' union is also under the general supervision of the Vice-Dean for education and students' affairs. As part of the Faculty of Engineering, the programmes' students are members in the union and have similar rights and benefits as the mainstream students, including entering the union's yearly elections.

### **c) Financial Affairs Administration**

The programmes' financial affairs administration, located at the Ground Floor of the New Educational building, is responsible for issuing the payment orders for the students' tuition fees at the beginning of each semester. The administration is also responsible for collecting the copies of the students' payment receipts, which should be presented by the students after making their payment at the Faculty treasury. Programmes' students who fail to present copies of the payment to the programmes' financial administration risk having no payment records at the programmes.

### **d) Library**

The Faculty library provides a service specially designed to fulfil the requirements of all academic programmes. It is open for all Faculty members for reference use and borrowing. The main library has a shelf space for over 40,700 books on all subjects forming part of the Faculty curriculum. It has 353 technical periodicals (the Faculty receives 23 periodicals yearly in a regular basis). Additionally, it has more than 3,340 Ph.D. and M.Sc. theses resulted from all Faculty departments' activities. The students' library has multiple copies of textbooks, amounting to over 13,000, available for short-term borrowing to students. According to the Engineering Faculties libraries

development project, annexed to the Ministry of Higher Education, the library is interconnected through the Internet with all the libraries of engineering faculties nationwide. VTLS library software system has been installed which contains all the modules to provide library services to the Faculty community.

**e) ASU - FoE Information Systems**

ASU - FoE have a solid understanding of the importance of information systems in each aspect in the CHEP academic environment. Hence, a comprehensive web portal has been created for CHEP that has all information and services needed for the student, parents, and staff members. Learning Management System (LMS) is one of the available service at the ASU - FoE portal for all students mainly to have their course materials posted regularly on it with a dedicated protected access to the courses he enrolled in them. More importantly, a comprehensive Student Information System (SIS) is another service that is available on the portal to all parties involved in the system. The student can use SIS to access academic records, course registration, request open courses that are not offered, or even request advising appointment with academic advisors.

## **9 STUDENT SUPPORT**

### **9.1 Local arrangements for academic and pastoral care for students**

#### **Induction**

Students' support and guidance are provided through a range of resources. A welcome and induction process starts in their first week, where all students are guided to their programme studies. Student induction and orientation takes place on the first day of each academic year. The purpose of induction is to introduce new students to their peers, the academic and support staff, to familiarize them with the access to and use of facilities and to outline the relevant Policies, Procedures, Rules and Regulations. Information on the programme, student support services and the teaching and learning philosophy adopted by the College is communicated verbally and in writing.

Currently, at the beginning of each programme, the faculty meets and greets the new cohort and addresses the following topics in an induction programme:

- (1) Programme Structure (how and when modules are assessed)
- (2) Programme Content
- (3) Assessment Grading
- (4) Attendance
- (5) Responsibilities they have in learning process – the importance of meeting assessment deadlines
- (6) Importance of presenting authentic work and being clear on what constitutes plagiarism rules
- (7) Appeals procedures
- (8) Allocation of Personal Tutors
- (9) Access to UEL electronic learning resources
- (10) Access to UEL Library and Learning Services
- (11) UEL Academic Framework
- (12) Assessment regulations
- (13) Extenuation

At the start of the programme each student will be given either a hard copy of the programme handbook or access to the VLE where this will be published.

#### **English language Support**

For those who require additional support in English language additional sessions are scheduled by ELTU (English Language Teaching Unit).

#### **Student Mentorship**

The Academic staff must provide each and every student with the support required to perform academically, and encourage active engagement from the students through:

- Establishing a supportive relationship with all students
- Adopting a creative approach to teaching and learning
- Providing regular constructive assessment feedback
- Mentoring and coaching

Students may make an appointment to meet with any tutor or the programme leader to discuss their progress and request additional assistance with managing their workload or to ask for additional tutoring in an area that she/he may be struggling with.

### **Academic Advisor**

All students enrol on the programme will be assigned an Academic Advisor (AA). This Academic Advisor will:

- Assist students with the process of induction and orientation into academic life and the University/College community and respond promptly to any communication from him/her;
- Work with students to build personal academic relationships;
- Retain an interest in their students' personal and general academic and professional development throughout their academic careers while at the University/College, providing information and guidance on academic choice;
- Monitor both academic performance and student engagement in a proactive manner and advise on constructive strategies to enable improvement, for example through the use of a personal portfolio or personal development plan;
- Listen and offer students help and advice about pastoral/non-academic matters and to signpost students to other student services for further assistance if necessary;
- Ensure that a note is kept of discussions at each meeting (with the student) and any follow-up actions agreed with the student;
- Provide references to students in their quest for employment or further study.

### **Academic Support Systems**

At ASU, students have full access to all required facilities and receive the best preparation for their undergraduate studies. These are including Library, Lab Room, ICT Room, Photocopying Facilities, etc. In addition, all students are assigned an Academic Advisor. Students participate in class activities that help develop their presentation and language skills, leadership skills, critical thinking skills and social skills, giving them greater confidence for their future academic challenges.

### **Teaching**

At the FoE, teaching follows university practice with lectures, tutorials, assignments, projects and in college tests designed by an experienced teaching team. The programme's learning management system is setup to have a page for each course studied during the semester. The student can access their courses from the main programme web-page. All electronic services provided to the students requires the use of university e-mail, hence, it is created automatically for the programme's student when they are first enrolled to the programme, and they retain this e-mail until they graduate.

### **Student Affairs**

At ASU there are Student Affairs Officers who offer friendly and caring support and mentorship to students, not just for academic matters but also for personal problems.

Throughout the programme, the Students' Affairs Officer organizes weekly meetings, business trips and outings to places of interest in and around Cairo, as well as international trips during the summer holiday.

## **Safe Environment**

FoE ASU provides a safe, caring and nurturing learning environment with friendly, supportive mentors and teachers who have many years of experience in teaching and mentoring.

Mental health support is provided by FoE- ASU Advising system. Advisors support students who are experiencing mental health problems that are affecting their ability to study. ASU has agreements with NOGs that help students to face study stress in addition to ASU 13 hospitals which may help students in such issues.

### **9.2 Technical support for learners and staff**

ASU employs a team of technical IT support and professional services staff to help staff and students with their teaching and assessment activities. The centre employs a dedicated IT Manager to provide the learners and staff with the necessary advice about the technical needs of the mode of study throughout the length of the programme. The students and staff have the full access to the ICT room, photocopiers, printers and e-library throughout the course of the term. The IT team provide learners and teaching staff with the necessary technical support in using 'Turnitin' software throughout the assignment submission and assessment process.

The team provides specialist technical support for teaching, learning and assessment activities to ensure they run smoothly. This can be anything from preparing resources, operating specialist laboratories and quantity surveying, to setting up classrooms.

Technical teams frequently have responsibility for related areas such as managing health and safety, contingency planning and capital planning, maintenance of both hardware and software.

### **9.3 Information on how the entitlements of disabled students have been addressed within curriculum design**

As a UEL validated programme, the curriculum has been designed to adequately address needs and requirements of disabled students. From a local perspective the programme team will ensure that if there are disabled students on the programme the following will apply:

- Step free access to laboratories/classes
- Larger fonts sizes for presentation materials
- The use of scribes
- Voice recorders will be allowed (with the permission of the presenting lecturer)
- Extra time for examinations
- Use of word processor (PC) without Internet access for examinations.
- Separate room for special needs students (if requested)

## **Equality and Diversity**

The curriculum has been designed to meet the needs of all undergraduate students, with all ages, genders, or learning / physical disabilities. There is a strong emphasis on work-based learning. By using a full range of assessment techniques this enables students with different learning styles to be accommodated for. ASU has a policy of designing an inclusive curriculum where appropriate adjustments are made to the design, delivering and assessment process to cater for students with any learning difficulties. Teaching materials and module content has been designed to be inclusive addressing the needs of our diverse student body. Teaching methods include lectures, seminars, tutorials, discussions and workshops to address the needs of diverse learning needs.

## **Access to UEL Academic Link Tutor (ALT)**

All ASU students on the proposed programmes (being submitted for approval) will have access to the respective Academic Link Tutor generally via email. Students are encouraged to discuss any issue or concerns with their in-house tutors at the first instance before contacting the Academic Link Tutor.

## **UEL Resources**

As UEL registered students, FoE - ASU students will also have access the following UEL resources:

- UEL Library including e-resources, databases and e-journals (subject to licence allowances)
- Study skills Plus – an online diagnostic and assessment tool which can help students develop their core English and maths skills.
- UEL Direct Information and communications technology (ICT) resources such as Office365

## **The role of the UEL Academic Partnership Office (APO)**

The APO will work in liaison with the ALT, however principally the role of the APO is administrative support for the ALT and the Partner. The APO will be the first point of contact for the partner and will channel concerns, issues, queries to all UEL Central Services such as Registry, Assessment Unit, The Hub, Courses and Systems, UEL Library and so on.

## **9.4 Student Feedback Mechanisms**

Student representatives will be either elected or nominated for each programme. These representatives are the means of formal communication to the various committees at FoE - ASU Campus and UEL. There will be two formal meetings per year with the student representatives, module leaders and the programme coordinator at FoE - ASU Campus. The External Examiner report will also be made available for students to access. The issues raised at these meetings will be communicated to the Academic Link Tutor or APO at UEL. Actions resulting from these issues will be

monitored and taken in the next committee meeting, where the representative will get an update, if not solved then and there.

We ask that student representatives discuss all matters informally with their Module Tutor at FoE - ASU before raising them at committee level. It should be possible to solve most problems by an informal approach. The earlier the programme team are made aware of any problems, the earlier FoE - ASU will attempt to correct problems. Student support is appreciated and acknowledged consistently in the student End-of-Module Evaluation Questionnaires and verbal feedback. The information collected from the Questionnaires is delivered to the Senior Management of FoE - ASU for analysis and taking any remedial actions.

## **Academic Progress**

Students on the double degree programme will be able to access their records/profile via UEL Direct. ASU also has its own The Student Information System (SIS) platform where students can access all their academic records. It can be reached on the main programme web-page, which also provides brief information about the mission and vision of the programme, and the important dates related to student academic activities. Students receive an Academic report on a quarterly basis to assist them to monitor their progress and to identify any areas of concern. Students also meet with the Academic Head and the relevant facilitators to discuss their progress. Recommendations for improvement are made and the feedback is minuted

## **Students with learning challenges**

Students with learning challenges are accommodated as far as possible, taking the current College resources into consideration. The Academic Board is responsible for approving any recommendations made by the Student Counselor to accommodate a student with any of the following learning challenges:

- A cognitive disadvantage which affects their ability to learn at the same rate as their peers.
- A specific learning difficulty which may or may not be linked to a cognitive disability
- A speech and language impairment affecting their ability to comprehend
- A physical disability and sensory impairment
- An emotional disability which can affect their ability to learn
- An extended period of absence which could occur for a variety of reasons
- A behavioral impairment affecting their ability to concentrate and therefore learn effectively
- Students who speak a different language at home than the one they speak at College

## **Online information and support:**

As previously mentioned, the programme team will use their own VLE. A bespoke section will be created for

- Induction information
- Academic support for students available both at FoE - ASU and UEL

- FoE - ASU Student Enquiries Desk opening hours
- FoE - ASU Library opening hours
- Link to UEL Library online resources
- Copy of Programme Handbook

Please refer to Appendix F for Student Entitlements, for support available at UEL.



## 10 RESOURCES

### a) Local library and IT resources

ASU - FoE central library serves students and researchers in various fields besides the Digital Library to provide an online service for users. There is (1) central library with (3) halls according to the following:

- The student library hall contains (16,461) books.
- The teaching staff hall contains (29,607) books.
- Digital Library Hall

The Digital Library serves to provide an online Service for users. It gives online access to the contents of the library, including books and theses. The digital library website: [http://srv2.eulc.edu.eg/eulc\\_v5/libraries/start.aspx](http://srv2.eulc.edu.eg/eulc_v5/libraries/start.aspx)

Other learning resources are the Egyptian Bank of Knowledge (EBK) through the website: <http://www.ekb.eg/> "Egyptian Knowledge Bank", is a presidential initiative started at 2016 and is one of the largest national projects that is concerned with education in Egypt, it aims to provide huge and diversified sources for knowledge and culture for free. It comes after contracting with several international publishing houses to give access to their contents in all scientific and cultural disciplines. Generally, 25 global publishing house and specialised companies, have their content at the Egyptian Knowledge Bank such as Emerald, John Wiley Elsevier etc. E-Mail Services involved a developed Cooperation of the University with Microsoft Corporation to Serve Undergraduate and Postgraduate Students offering new features for the official e-mail users.

### b) Other local resources relevant to supporting the programme

The faculty offers students Training Support through **Global Training Technology Centre**. It aims to be a centre for innovation in technology and entrepreneurship, as to form a link between academic study and labour market. The centre offers training programmes to serve students and graduates at the same time, these training programmes aim to develop the creative sense of the trainees in order to integrate them into creative and innovative works that would serve the industrial field and the community. Depends on the overlap between the different disciplines in various fields and at various levels. The centre is nearly 1000 m<sup>2</sup> area, it works as the headquarters for the students to practice their activities in the future, and the college is preparing the headquarters of the centre to accommodate the necessary training activities.

**Employability and Career Development Centre (ECDC)** is a Centre constructed through the collaboration between Ain Shams University and the American University, it has a permanent headquarter in Faculty of Engineering and another headquarter in Ain Shams University. It provides special training programmes for students in order to develop their capabilities in the professional and employment fields. The centre aims to guide the trainee to his excellence and weaknesses points, and how to raise points of excellence and overcome weaknesses.

The number of computers available to students is about 600 modern machines. A suitable number of computers are available for faculty members in their respective

laboratories and offices in different sections. The number of computers available to employees is 250 devices. Computer labs are run centrally for students. The method of using these labs has been adopted by setting a nominal fee of not less than two pounds per hour to use the central labs which are open to access the network, while the student does not bear any burdens to enter the laboratories associated with the ministry while the Income is suitable for the maintenance and modernization of computers in college. The databases and information systems of faculty staff members, their assistants, students, graduate students, expatriates, administrators and libraries have been developed and updated. The databases are continuously updated.

The Faculty of Engineering has a website through the main website of Ain Shams University. The website is: <https://eng.asu.edu.eg/> . The website provides various services for students and faculty members by presenting the internal regulations of the bachelor's degree course as well as higher education. The site is being developed and data recorded within it are consistently updated. The contents of the various educational materials are displayed. The course schedules and exam results are announced at the end of the semester. The site is available in Arabic and English so that the user can choose the appropriate language. This site is regularly updated by site administrators and college administration. E-mail access is also available to the faculty members and the assistant staff and the students on the website of the College.

In order to update the educational services to the international standards, an online portal was developed in order to open the access to students and staff members to perform efficiently online. Students can view their courses, submit coursework and view their grades. Staff members can upload their lectures, view the online submissions and grade online. An information technology unit was set up for the electronic portal of the college to be the main focus of interaction between students and faculty.

Library and Learning Services at UEL have excellent range of resources and facilities to help the students with their studies. students can access the Library and Learning Services at UEL using the following link: <https://www.uel.ac.uk/lis/>

## **11 INFORMATION ABOUT QUALITY AND STANDARDS**

### **11.1 Assuring the quality and standards of the award**

You are enrolled on a programme of study leading to the award of a degree of the University of East London (UEL). As such, you are regarded as a student of the University of East London as well as ASU- FoE and both institutions work together to ensure the quality and standards of the programme on which you are registered. The final responsibility for all quality assurance, validation and standards' matters rests with UEL.

Some of the ways in which we ensure the quality and standards of the programme include:

#### **Approval of the programme and institution at which you are studying**

Before the programme started, our University, through an approval process, checked that:

- there would be enough qualified staff to teach the programme;
- adequate resources would be in place;
- the overall aims and objectives were appropriate;
- the content of the programme met national benchmark requirements, where applicable
- the programme met any professional/statutory body requirements if applicable;
- the proposal met other internal quality criteria covering a range of issues such as admissions policy, teaching, learning and assessment strategy and student support mechanisms.

#### **Appointment of external examiners**

The standard of this programme is monitored by at least one external examiner external to UEL, appointed by UEL. External examiners have two primary responsibilities:

- To ensure the standard of the programme;
- To ensure that justice is done to all students.

#### **External examiners fulfil these responsibilities in a variety of ways including:**

- Approving exam papers/assignments;
- Attending assessment boards;
- Reviewing samples of student work and moderating standards;
- Ensuring that regulations are followed;
- Providing feedback to the University through an annual report that enables us to make improvements for the future.

#### **Review and Enhancement Process**

- This annual review includes the evaluation of and the development of an action plan based on:
- external examiner reports and accreditation reports (considering quality and standards);
- statistical information (considering issues such as the pass rate);

- student feedback obtained via programme committee and module evaluation questionnaires.

Periodic reviews of the partnership and programme

- This is undertaken by a panel that includes at least two external subject specialists. The panel considers documents, looks at student work, speaks to students and speaks to staff before drawing its conclusions.

## **Award certificates**

Issuing transcripts of results to students, and award certificates to successful students on programmers.

The student who achieves an accumulative GPA of 3.6 or higher after any semester and did not fail any course throughout his course of study is included in the Dean's List and receives partial exemption from charges on the next semester. This exemption is dependent on the student's GPA as recommended by the Programme Administration Council in this regard and after approval of the Council of the Faculty of Engineering.

Students who complete 480 credits, graduate with an Honours Degree, which is documented in their graduation certificate. The faculty sets a system for encouraging distinguished students through reducing their tuition fees in accordance with their academic performance. At the beginning of each semester, the distinguished students' list is announced together with the associated tuition fees reductions.

Students who manage to fulfil all graduation requirement are awarded a double degree from ASU – FoE in Communication Systems Engineering programme.

## **Equality and Diversity**

ASU Equality and Diversity Strategy

- ASU commits to ensuring equality and diversity in its campus. Equality is ensured for everyone regardless any grounds of discrimination such as gender, age, colour, disability and religion.
- The university supports a safe environment for both working and studying. The university environment must be free of bullying, harassment, and any form of discrimination. Any act of the aforementioned will not be tolerated, and any complaints will be taken seriously. Anyone who feels being subjected to these acts is encouraged to raise complaints.
- All academic staff members, students and employees are supposed to treat each other with mutual respect and fairness. Everyone should respect the presence of individual differences, diversity in culture, personal opinions and beliefs.
- Equal opportunities and access to facilities are allowed for all staff and students. Each staff member or student is given full support to develop their skills and talents. Selection for employment, promotion, training, or any other benefits will be based on aptitude and ability.

**UEL Equality and Diversity Strategy**

***<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies>*** (for all general policies)

## 12 ACADEMIC APPEALS

- 12.1 Students who wish to appeal against a decision of an Assessment Board may appeal in accordance with the procedure for Appeals against Assessment Board decisions (Manual of General Regulations: Part 7 Appeals Against Assessment Board Decisions).
- 12.2 Disagreement with the academic judgement of a Board of Examiners' decision cannot, in itself constitute a reason to Appeal. Academic judgement is a judgement that is made about a matter where only the opinion of an academic expert will suffice. For example, a judgement about assessment or degree classification or a judgement about a decision where a student is required to repeat or take further assessment will usually be academic judgement, and a student cannot appeal simply because they believe they ought to have received a higher grade or mark. For further information on the scope of this procedure, please refer to section 4 of Part 7 of the Manual of General Regulations.
- 12.3 Students are strongly advised to make every reasonable effort to resolve their appeal informally, through meeting with the member of staff most directly concerned with the matter, such as the Programme or Module Leader, before proceeding to submission of a formal Academic Appeal. At open conciliation stage the appeal should be raised as soon as possible and normally no more than 10 working days after the publication of relevant assessment results via UEL Direct.
- 12.4 Further information about the UEL appeals process, including copies of the formal Notification of Appeal Form, is available for view at ***<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Student-Appeals>***
- 12.5 To help you decide whether your query would be an Appeal or Complaint, please refer to ***<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies>***
- 12.6 If you would like to lodge a formal appeal or have any queries, please email the Institutional Compliance Office at ***[appeals@uel.ac.uk](mailto:appeals@uel.ac.uk)***

## 13 COMPLAINTS

- 13.1 If you feel that you have not received the standard of service which it would be reasonable to expect, you may be entitled to lodge a complaint, in accordance with section 14 of the *Manual of General Regulations*. The Complaints Procedure should be used for serious matters, and not for minor things such as occasional lapses of good manners or disputes of a private nature between staff and students. A complaint may be submitted collectively by a group of students who should nominate a spokesperson who will be the channel of communication for the group, however, a complaint may not be lodged by a third party on behalf of the complainant. The complaints procedure is an internal process.
- 13.2 Separate procedures exist for the following, which therefore cannot form the substance of a complaint:
- appeals against the decisions of Assessment Boards (see Part 7 of the Manual of General Regulations);
  - appeals against annual monitoring reviews, transfer of research degree registration or oral examination decision for postgraduate research students (see Part 9 of the Manual of General Regulations);
  - appeals against the decisions of the Extenuation Panel (see Part 6 of the Manual of General Regulations);
  - complaints against the Students' Union (see the Complaints Procedure in the Students' Union constitution);
  - appeals against decisions taken under disciplinary proceedings (see Part 12 of the Manual of General Regulations);
  - complaints about businesses operating on University premises, but not owned by our university (contact the Deputy Vice-Chancellor and Chief Operating Officer);
  - complaints about the behaviour of other students (see Part 12 of the Manual of General Regulations this Manual);
  - appeals against the decisions of Academic Misconduct Panels (see Part 8 of the Manual of General Regulations)
  - appeals against the decisions of Attendance Appeal Panels (see the University's Attendance Policy).
- 13.3 Students wishing to submit a complaint must, in the first instance, follow the complaints policy of ASU - FoE which aligns to the Office of the Independent Adjudicator's good practice framework (<https://www.oiahe.org.uk/media/96361/oia-good-practice-framework.pdf>).
- The ASU - FoE complaints policy is available at:  
<https://eng.asu.edu.eg/front/bylaws/show/appeals>  
[https://eng.asu.edu.eg/uploads/uploadcenter/asu\\_337\\_file.pdf](https://eng.asu.edu.eg/uploads/uploadcenter/asu_337_file.pdf)
- 13.4 ASU- FoE will administer all stages of its complaints policy and, upon exhaustion of this policy, will issue a formal letter to the complainant notifying them that its complaints policy has been exhausted. If the complainant is still not satisfied with

the outcome they will be entitled to request that the University of East London undertake a review of their complaint.

- 13.5 The University of East London will conduct a review of the complaint in accordance with Stage 3 of its own Complaints Procedure. The University of East London Complaints Procedure is available at:

***<https://www.uel.ac.uk/discover/governance/policies-regulations-corporate-documents/student-policies/manual-of-general-regulations>***

- 13.6 The University of East London will administer the Stage 3 review in accordance with its Complaints Procedure and, upon completion of the review, will issue a Completion of Procedures Letter. If the complainant is still not satisfied with the outcome they will be entitled to make a complaint to the Office of the Independent Adjudicator.
- 13.7 Complainants are strongly advised to make every reasonable effort to resolve their complaint informally through meeting with the member of ASU - FoE staff most directly concerned with the matter, such as the Programme or Module Leader, before submitting a formal complaint.
- 13.8 Complaints must normally be lodged within the set time limits outlined in the relevant complaints policy. This ensures that the people involved still remember the case, and the facts can be established.
- 13.9 If you would like to request that the University of East London undertake a review, following the exhaustion of the ASU – FoE complaints policy, please email the Institutional Compliance Office at ***[complaints@uel.ac.uk](mailto:complaints@uel.ac.uk)***



## 14 EXTENUATION

General Information about extenuation can be found at  
**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Extenuation-Procedures>**

The University of East London has agreed, through Academic Board, procedures governing extenuation for students concerning the assessment process.

The BEng (Hons) Communication Systems Engineering Students programme will be subject to equivalent procedures, with the process being administered by, and the panel being held within Ain Shams University – Faculty of Engineering

If granted by the panel, **Extenuation can**

- (i) Allow students to hand in coursework up to 7 days late.

**or**

- (ii) Allow students to proceed to their next attempt uncapped.

**Extenuation doesn't**

- (i) Give students more attempts to pass a module
- (ii) Reschedule exams
- (iii) Uncap a capped module
- (iv) Give students a higher mark.
- (v) Allow students to hand in work over 7 days late.

The basic principle is that extenuation should put you in the same position that you would have been in had you not missed the exam or handed in the assessment late – it does not confer any advantages.

UEL decided that its procedures would be

- Evidentially based
- Handled centrally by an panel of senior staff (not devolved to various parts of the organisation)
- Retain student anonymity where possible

The extenuation procedures are intended to be used rarely by students not as a matter of course.

The procedures govern circumstances which

- Impair the performance of a student in assessment or reassessment
- Prevent a student from attending for assessment or reassessment
- Prevent a student from submitting assessed or reassessed work by the scheduled date

Such circumstances would normally be

- Unforeseeable - in that the student could have no prior knowledge of the event concerned

- Unpreventable - in that the student could do nothing reasonably in their power to prevent such an event
- Expected to have a serious impact

Examples of circumstances which would normally be regarded as serious are:

- *A serious personal illness* (which is not a permanent medical condition – this is governed by disability procedures)
- *The death of a close relative immediately prior to the date of assessment*

Examples of circumstances which would *not* normally be regarded as extenuating circumstances are:

- Failure of computer equipment / USB stick
- Transport problems, traffic jams, train delays
- Misreading the exam timetables / assessment dates
- Minor illnesses

The judgement as to whether extenuation is granted is made by a panel of senior persons in the organisation who make this judgement on the basis of the evidence the student provides (not on their knowledge of the student) – where possible the identity of the student is not made available to the panel. The judgement is made on the basis that the circumstances could reasonably be thought to be the sort of circumstances which would impair the performance of the student etc. The actual performance of the student is not considered and is not available to the panel.

It is the responsibility of the student to notify the panel, with independent evidential documentary support, of their claim for extenuation.

More information and student guidance notes can be found at:

**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Extenuation-Procedures>**

### **14.1 Seeking Advice: Academic Advisor**

Every student is assigned an Academic Advisor who is one of the faculty members and may continue with the student for the whole study duration.

The Academic Advisor should follow-up with the student, assist him in selecting courses each semester.

The Academic Advisor may ask the student to repeat courses which he already passed or ask him to register in additional courses to raise his accumulative GPA to that required for graduation

## ACADEMIC CALENDAR 2019-2020

## APPENDIX A



# Academic Calendar 2019/2020

Semester	Activity	From	To
First Semester Fall 2019	Course Registration	07/09/2019	19/09/2019
	Classes	21/09/2019	02/01/2020
	Adding Courses	21/09/2019	26/09/2019
	Dropping Courses	21/09/2019	03/10/2019
	Midterm Exams	08/11/2019	15/11/2019
	Withdraw Courses	19/10/2019	28/11/2019
	Final Exams	04/01/2020	24/01/2020
	Break	25/01/2020	06/02/2020
Second Semester Spring 2020	Course Registration	01/02/2020	06/02/2020
	Classes	08/02/2020	21/05/2020
	Adding Courses	08/02/2020	13/02/2020
	Dropping Courses	08/02/2020	20/02/2020
	Midterm Exams	27/03/2020	03/04/2020
	Withdraw Courses	07/03/2020	16/04/2020
	Final Exams	27/05/2020	19/06/2020
	Break	20/06/2020	09/07/2020
Summer 2020 Semester	Course Registration	27/06/2020	02/07/2020
	Classes	04/07/2020	20/08/2020
	Adding Courses	04/07/2020	06/07/2020
	Dropping Courses	04/07/2020	09/07/2020
	Withdraw Courses	08/08/2020	13/08/2020
	Final Exams	21/08/2020	28/08/2020
	Break	29/08/2020	17/09/2020
Start of Academic Year 2020/2021		19/09/2020	

## USEFUL WEB PAGES

## APPENDIX B

Academic Appeals

**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Student-Appeals>**

Academic Integrity Policy

**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies>**

Accreditation of Experiential Learning

**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Manual-of-General-Regulations>**

(Manual of General Regulations – Part 2 – Admission of Students)

Assessment and Feedback Policy

**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Assessment-and-Feedback-Policy>**

Civic Engagement

**<https://www.uel.ac.uk/Connect/Civic-Engagement>**

Complaints procedure

**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Student-Complaint-Procedure>**

Equality and Diversity Strategy

**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies>**

(for all general policies)

Extenuating Procedures

**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Extenuation-Procedures>**

Library and Learning Services

**<https://www.uel.ac.uk/lis/>**

Manual of General Regulations

**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Manual-of-General-Regulations>**

Referencing guidelines

**<https://uelac.sharepoint.com/LibraryandLearningServices/Pages/Harvard-Referencing-.aspx>**

Skills Curriculum

**<https://www.uel.ac.uk/discover/governance/policies-regulations-corporate-documents/student-policies/skills-curriculum>**

Skills Portal

**<https://uelac.sharepoint.com/LibraryandLearningServices/Pages/Skillzone.aspx>**

Suitability Procedures

**<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Manual-of-General-Regulations>**  
(Manual of General Regulations – Part 13 – Suitability Procedure)

Write it Right **<http://writeitright.uelconnect.org.uk/>**

UEL Intranet (UEL ID required to login) **<https://www.uel.ac.uk/students>**

**Student Attendance and Engagement Policy – Guidance for Students****Teaching Policy**

**Language:** English language should be used for lecturing, discussions, exams, and all verbal and electronic communications. Use of Arabic language is strictly forbidden even in one-to-one conversation between the instructor and the students.

**Course Syllabus:** Each course syllabus should contain: course objectives, textbook, outline, material, assessments, grading policy and outcome. Outline should contain sections covered every week with reference to chapters/sections in the textbook. The instructor should give the course syllabus to the students in the first class. The syllabus serves as a contract between the instructor and the students.

**Textbook:** The instructor is free to select/recommend a textbook but it should be international and available. The textbook information should be provided to the administration office or the unit head before the first class of the course.

**Attendance:** Attendance is taken in lecture and tutorial classes. It is assigned a percentage based on the grading policy. Students should not be allowed to enter the class after 5 minutes from the scheduled time. No eating, drinking, or mobile use in the class. If the student wants to leave the class for any reason, he will not be allowed to come back to the class. The student's attendance should not be less than 75% during the course. Otherwise, the student should not be allowed to attend the final exam.

**Assignments:** Assignments are given every week (spelled out in the course syllabus), preferably from the textbook. Assignments should constitute 20% of the total grade. Instructors are allowed to drop the least assignment from the grade. The assignment is collected at the end of the tutorial period of the next week. Instructors may grade only selected problems from the assignment. The graded assignment should be returned and discussed with the class.

**Quizzes:** Unannounced quizzes are given in the tutorials to force the students to study and be ready all time. These quizzes should constitute 10% of the total grade. The quiz is given at the end of the session for 15 minutes max. Up to 6 quizzes can be given and the least one can be dropped from the grade. The graded quiz and the model answer should be returned the following tutorial and discussed with the class.

**Exams:** One midterm exam should be given. Time should be indicated in the course syllabus. The midterm exam should be given during the 6th - 7th week. This exam will be held during lectures/tutorials based on course progress and will constitute 25% of the grade. The graded midterm exam and its model answer should be returned and discussed with the class. The instructor can arrange for a bigger or more suitable room for the midterm exam. The final

exam constitutes 40% of the grade. It should be a comprehensive exam covering all material. The student fails the course if he gets less than 30% of the final exam total grade. Instructors may select to have all exams open-book or closed-book.



**UNIVERSITY OF EAST LONDON**

**TITLE: PROGRAMME COMMITTEE (COLLABORATIVE)**

**TERMS OF REFERENCE**

To be responsible for assuring and enhancing the quality of the student experience at programme level by:

- Providing a forum in which students can express their views about the management of the programme, and the content, delivery and assessment of modules, or equivalent, in order to identify appropriate actions to be taken in response to the issues raised and to ensure that the implementation of these actions is tracked.
- Providing formal yearly student feedback on the programme as input into the preparation of the Programme REP.
- Reviewing programme questionnaire results and making recommendations and changes arising from these.
- Receiving, considering and approving the Programme REP and identifying responsibilities for action to be taken before it is considered by School Learning and Teaching Quality Committee.
- Reviewing progress on REP action plans at each meeting.
- Reviewing the relevant documentation and other evidence prepared for Academic and collaborative Institutional Review and other external review processes.
- Reviewing proposals for modification of the programme structure (validated programmes only) and noting implementation arrangements for modifications.
- Advising the Programme Leader on mechanisms by which University policy statements, which have an impact on programme design and delivery, are implemented.

**MEMBERSHIP**

Programme Leader (Chair)

Administrator/Servicing Officer (ex-officio)

Programme staff making a significant teaching contribution to the programme

Learning Support Services representative

Technician representative (for laboratory based programmes)

Dean of School/department or equivalent (ex officio)

UEL Dean of School/Associate Dean of School, or equivalent (ex officio)

UEL link person (ex officio)

Two student representatives for each level and at least one part-time student (where appropriate)

The meeting will be held once per semester/term and will be quorate if 40% of the members are present.

### ACADEMIC MISCONDUCT

For the purposes of university's regulations, academic misconduct is defined as any type of cheating in an assessment for the purposes of achieving personal gain. Examples of such misconduct are given below: the list is not exhaustive and the use of any form of unfair or dishonest practice in assessment can be considered potential misconduct.

#### Coursework Submitted for Assessment

For coursework submissions, academic misconduct means:

- (a) The presentation of another person's work as one's own with or without obtaining permission to use it.
- (b) The inclusion within one's own work of material (written, visual or oral), originally produced by another person, without suitable acknowledgment.
- (c) The submission, as if it were one's own work, of anything which has been offered to you for your use, but which is actually not your own work.
- (d) The inclusion within one's work of concepts paraphrased from elsewhere without citing your source.
- (e) The inclusion in submitted work of sections of text, whether from electronic or hard copy sources, without appropriate acknowledgement of the source.
- (f) The submission of work that the student, as the author, has previously submitted, without suitable acknowledgement of the source of their previous work; this should not normally be more than a short quotation as the same work cannot be submitted for different assignments.
- (g) Including or quoting the work of other students in one's work, with the exception of published work, or outputs held in the library as a learning resource, which should be cited and acknowledged appropriately.
- (h) Being party to any arrangement whereby the work of one candidate is represented as that of another.
- (i) The submission, as your own work, of any work that has been purchased, or otherwise obtained from others, whether this is from other students, online services, "cheat sites", or other agents or sources that sell or provide assignments.
- (j) Practices such as 'cutting and pasting' segments of text into your work, without citing the source of each.
- (k) For work not intended to be submitted as a collaborative assignment: producing work with one or more other students, using study practices that

mean the submitted work is nearly identical, overall or in part, to that of other students.

- (l) Offering an inducement to staff and/or other persons connected with assessment.

### Examinations

For examinations, academic misconduct means:

- (a) Importation into an examination room of materials or devices other than those which are specifically permitted under the regulations applying to the examination in question.
- (b) Reference to such materials (whether written or electronically recorded) during the period of the examination, whether or not such reference is made within the examination room.
- (c) Refusing, when asked, to surrender any materials requested by an invigilator.
- (d) The application of an electronic device, unless this has been expressly permitted for that examination.
- (e) Copying the work of another candidate.
- (f) Disruptive behaviour during examination or assessment.
- (g) Obtaining or seeking to obtain access to unseen examination questions prior to the examination.
- (h) Failure to observe the instructions of a person invigilating an examination, or seeking to intimidate such a person.
- (i) Offering an inducement to invigilators and/or staff and/or other persons connected with assessment.

Where academic misconduct is suspected, the matter will be dealt with under the *Procedure to be followed in the event of a suspected case of academic misconduct, Part 8, paragraph 4 (or, for postgraduate research students, Appendix I)* of the Manual of General Regulations (available for view at **<https://www.uel.ac.uk/Discover/Governance/Policies-Regulations-Corporate-documents/Student-Policies/Manual-of-General-Regulations>** ). If it is determined that academic misconduct has taken place, a range of penalties may be prescribed which includes expulsion from the programme.

## PLAGIARISM - A GUIDANCE NOTE FOR STUDENTS

### 1. **Definition of Plagiarism**

Our University defines plagiarism and other academic misconduct in Part 8 of the UEL Manual of General Regulations (to which all students are referred upon joining UEL), which is reprinted in "The Essential Guide to the University of East London". In this document, the following example of an assessment offence is given:

The submission of material (written, visual or oral), originally produced by another person or persons or oneself, without due acknowledgement\*, so that the work could be assumed to be the student's own. For the purposes of these Regulations, this includes incorporation of significant extracts or elements taken from the work of (an)other(s) or oneself, without acknowledgement or reference\*, and the submission of work produced in collaboration for an assignment based on the assessment of individual work. (Such misconduct is typically described as plagiarism and collusion.)

The following note is attached:

\*(Note: To avoid potential misunderstanding, any phrase that is not the student's own or is submitted by the student for a different assessment should normally be in quotation marks or highlighted in some other way. It should also be noted that the incorporation of *significant* elements of (an) other(s) work or of one's own work submitted for a different assessment, even with acknowledgement or reference, is unacceptable academic practice and will normally result in failure of that item or stage of assessment.)

### 2. **Plagiarism in Greater Detail**

Work that students submit for assessment will inevitably build upon ideas that they have read about or have learnt about in lectures. That is perfectly acceptable, provided that sources are appropriately acknowledged. It should be noted, however, that the wholesale reproduction of the ideas and words of others, however well referenced, is likely to lead to failure at assessment (see section 6 below)

The submission of work that borrows ideas, words, diagrams, or anything else from another source (or sources), without appropriate acknowledgement, constitutes plagiarism. Plagiarism is not limited to unattributed cutting-and-pasting; it includes the reproduction, without acknowledgement, of someone else's work, taken from a published (or unpublished) article, a book, a website, a friend's (or anybody else's) assignment, or any other source.

When an assignment or report uses information from other sources, the student must carefully acknowledge exactly what, where and how s/he

has used them. If someone else's words are used, they must be within quotation marks and a reference must follow the quotation. (See section 6 for further guidance on referencing.)

Where a concept or argument in another source is paraphrased (rather than directly quoted), quotation marks should not be used, but it will still be necessary to acknowledge the source. Remember, however, that the making of simple changes to the wording of a source, while retaining the broad structure, organisation, content and/or phraseology of the source, is unacceptable academic practice and will probably be regarded as plagiarism. (For helpful tips on how to avoid plagiarism, see "The Study Skills Handbook" by Dr Stella Cottrell, pages 122-125.)

### **3. Collusion**

Collusion is the term used to describe any form of joint effort intended to deceive an assessor as to who was actually responsible for producing the material submitted for assessment. Clearly, students are encouraged to discuss assignments with their peers, but each student must always ensure that, where an individual assignment is specified, the report/essay submitted is entirely the student's own. Students should, therefore, never lend work (in hard or electronic copy) to friends. If that work is subsequently plagiarised by a "friend", an act of friendship might lead to a charge of collusion.

### **4. When to Reference**

Our regulations do not distinguish between deliberate and accidental plagiarism, but you will not be accused of plagiarism, provided that you properly reference everything in your work that was said, written, drawn, or otherwise created by somebody else.

You need to provide a reference:

- when you are using or referring to somebody else's words or ideas from an article, book, newspaper, TV programme, film, web page, letter or any other medium;
- when you use information gained from an exchange of correspondence or emails with another person or through an interview or in conversation;
- when you copy the exact words or a unique phrase from somewhere;
- when you reprint any diagrams, illustrations, or photographs.

**You do not need to reference:**

- when you are writing of your own experience, your own observations, your own thoughts or insights or offering your own conclusions on a subject;
- when you are using what is judged to be common knowledge (common sense observations, shared information within your subject area, generally accepted facts etc.) As a test of this, material is probably common knowledge if
  - you find the same information undocumented in other sources;
  - it is information you expect your readers to be familiar with;
  - the information could be easily found in general reference sources.

## 5. **How to Reference**

Our University has agreed on a single version of the Harvard referencing system (the School of Psychology uses the American Psychological Association (APA) referencing style) and this (along with APA) can be found in *Cite Them Right*:

Pears, R. and Shields, G (2013) *Cite Them Right*. Newcastle: Pear Tree Press

*Cite Them Right* is available on line and hard copies can be found in our libraries and bookshops

## 6. **Plagiarism, or Unacceptable Academic Practice?**

If work that you submit for assessment includes substantial and significant elements of other sources and all of those sources are appropriately acknowledged, you will not have plagiarised, but you will be culpable of unacceptable academic practice, because there will be too little of your “own voice” to allow your knowledge to be assessed. Work that you submit for assessment must:

- use your own words;
  - provide a critical commentary on existing literature;
  - aim for novelty and originality;
  - demonstrate your understanding of the subject area by paraphrasing.
- Work that does not meet those criteria will fail.

**COLLABORATIVE STUDENT ENTITLEMENTS AT UEL** **APPENDIX F**

*[Please append the student entitlement letter provided by UEL]*



**HEALTH AND SAFETY**

- One of the principle roles of Ain Shams University administration is controlling dangers and risks. The University is aware that failures in health and safety administration can possibly prompt loss of life, injury, and damage to the University properties.
- According to the University, a fundamental standard of the Health and Safety policy is that it is in the hands of the individuals who cause the dangers and risks to manage and control them.
- The University appoints persons “capable to advice” to help with identifying, recognizing and controlling health and security dangers and risks. They may work in any sector of the University.
- Each College of the University holds a responsibility regarding the management and use of its own health and security policies and strategies. Despite that, the University and Colleges are still obliged to coordinate on the mutual matters of health and security which affect the more extensive University community.
- Heads of the different Departments must set out their own organizational courses of action for the safety measures. In addition, they abide by the general University Health and Safety Policies and are responsible for their implementation and management in their own departments and domains of responsibility.
- Each Head of Department might set up a Departmental Safety Policy, which works hand in hand with this University Health and Safety Policy to satisfy the prerequisite Health and Safety at Work measures.
- Each Head of Department must guarantee that everybody who might be influenced by the activities of the Department, knows about the health and security policies and arrangements, and has sufficient knowledge, information, time, preparation and supervision authority to allow for the identification, recognition and control of the dangers and risks to health and security.
- The supervisor of any departmental activity (field trip, practical work, office work or teaching activities) must have a comprehensive understanding of the related dangers and risks and conduct the risk assessment suitable for the circumstances of the activity. This is to fulfil the requirements of the Health and Safety at Work Regulations and different measures which state that no work might be attempted unless reasonable and adequate risk assessment has been done to define a safe and secure system of work.
- All University staff members are expected to be fully aware of both the University and Department policies and know that they hold the responsibility of this aspect for all those under their supervision or

management. This implies ensuring and promoting good working practices and environment. It also includes ensuring that practical and office work is done in safe spaces, equipment being maintained and checked in safe procedures, that the policies and strategies are being implemented and disseminated and that immediate reporting of any accidents or dangers takes place in order to take the necessary measures.

- The health and safety policy is also abiding to any private body or entity working inside the University premises. They must coordinate with the University on all matters related to health and safety management.